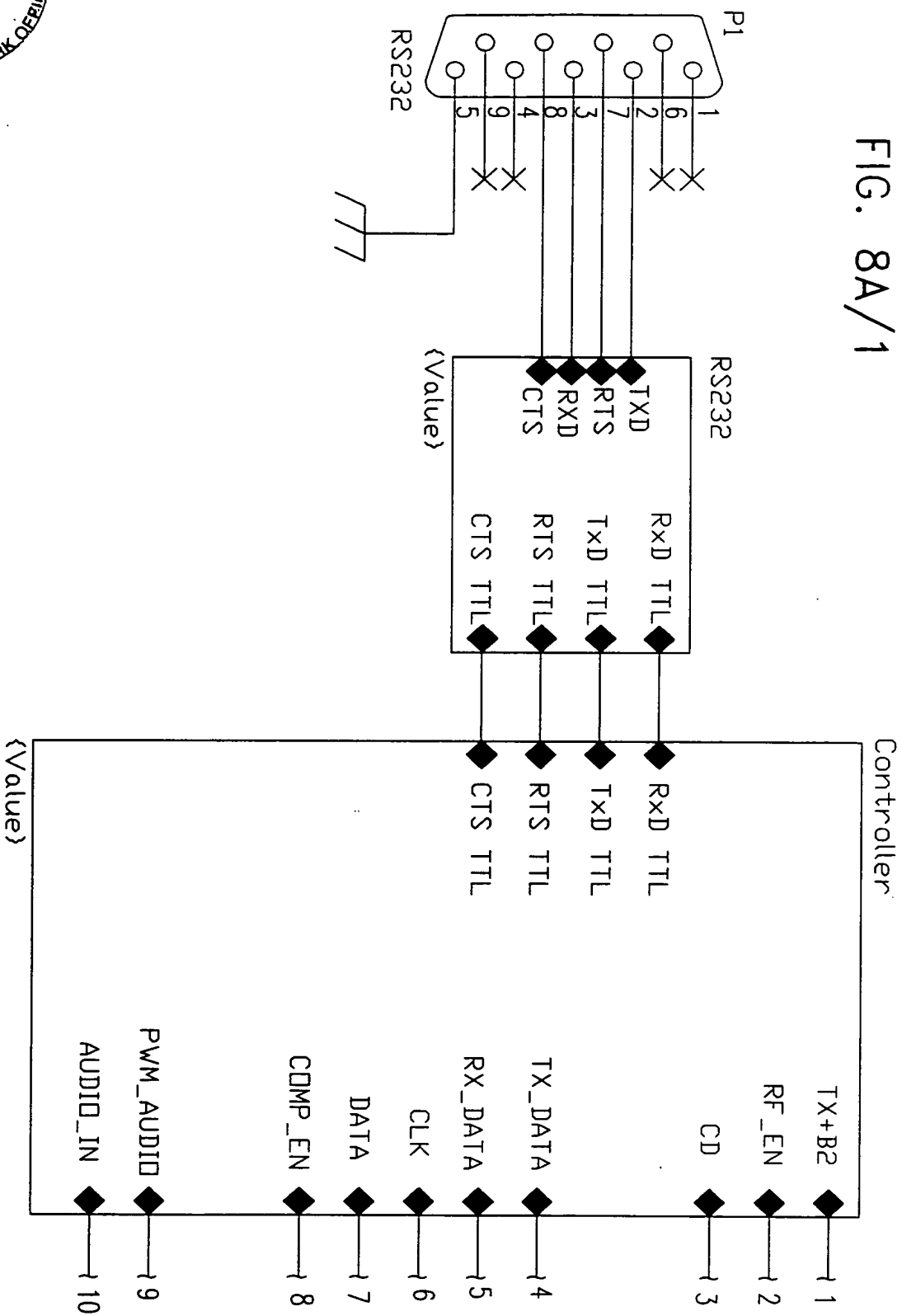


FIG. 8A/1



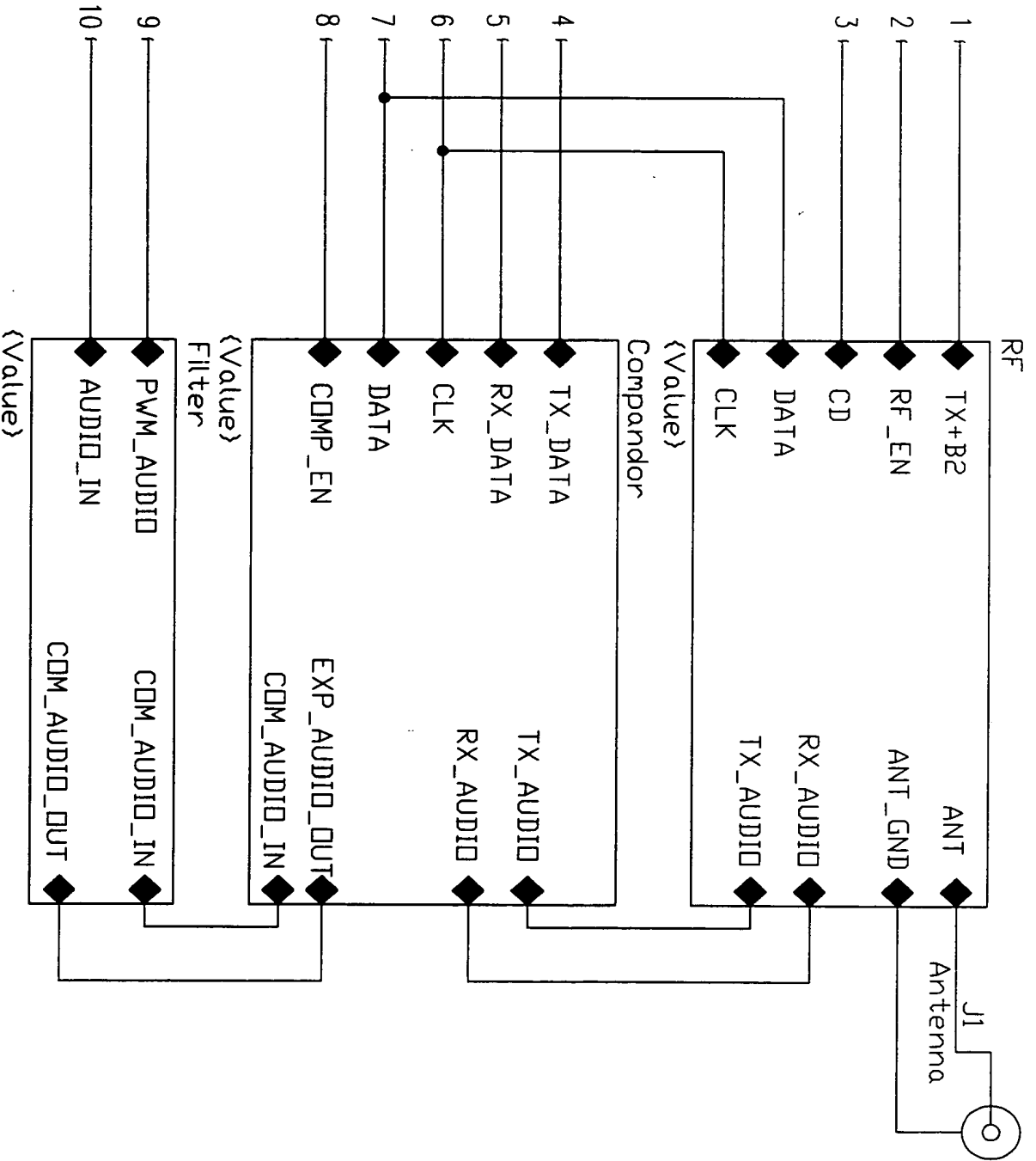


FIG. 8A/2

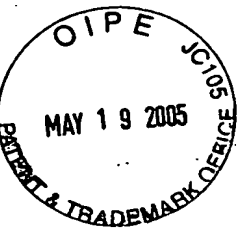


FIG. 8B/1

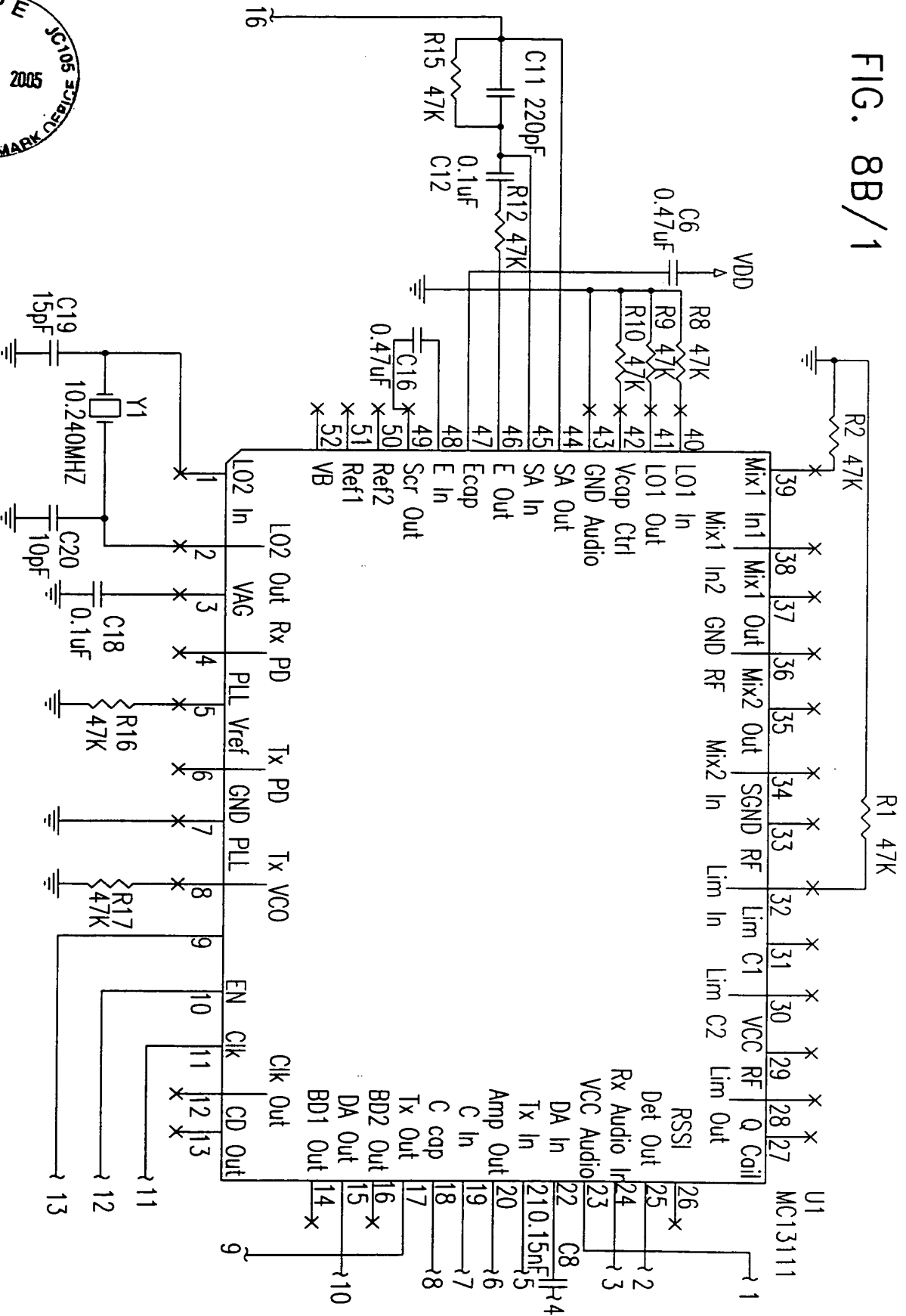
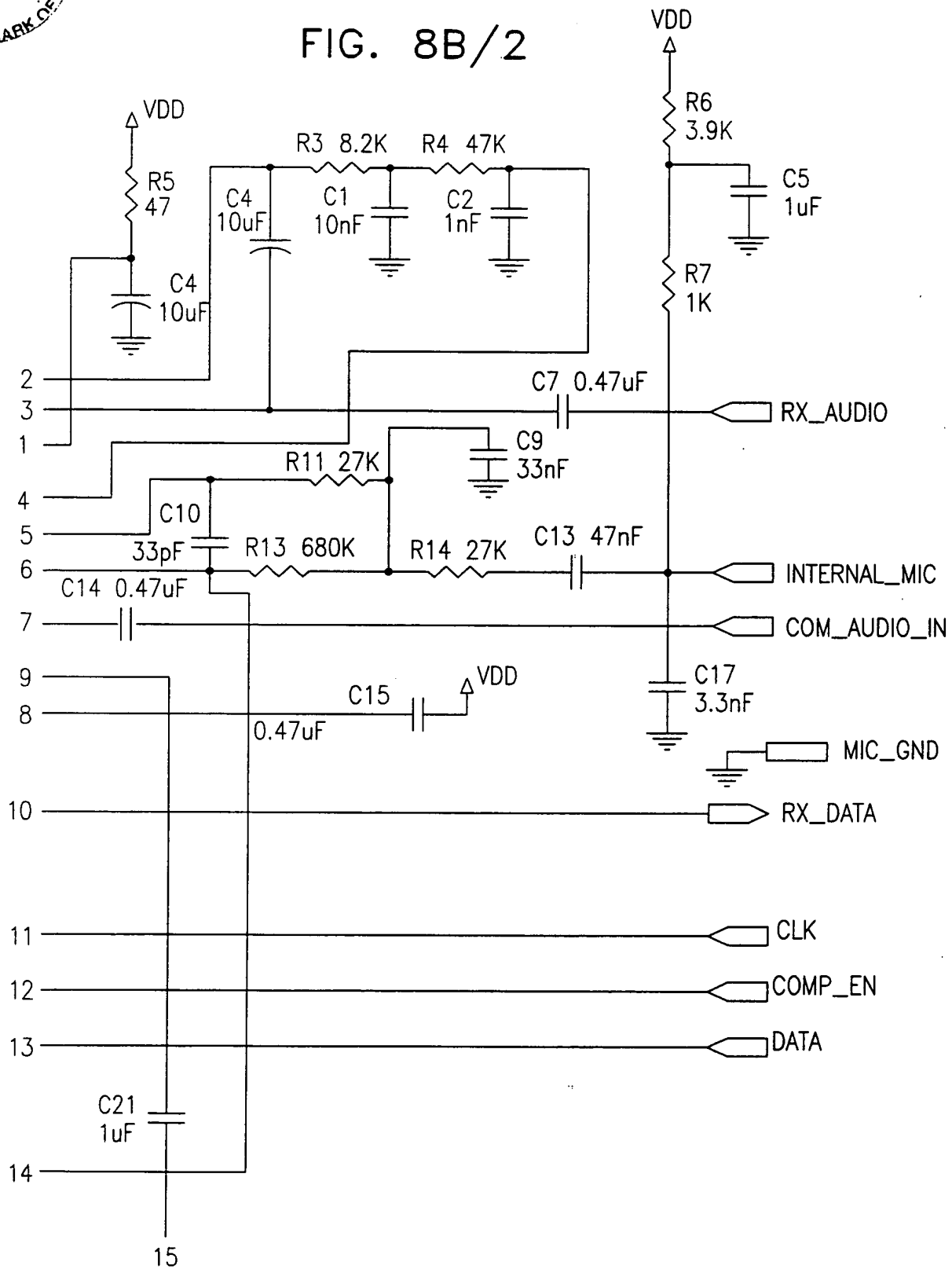




FIG. 8B/2



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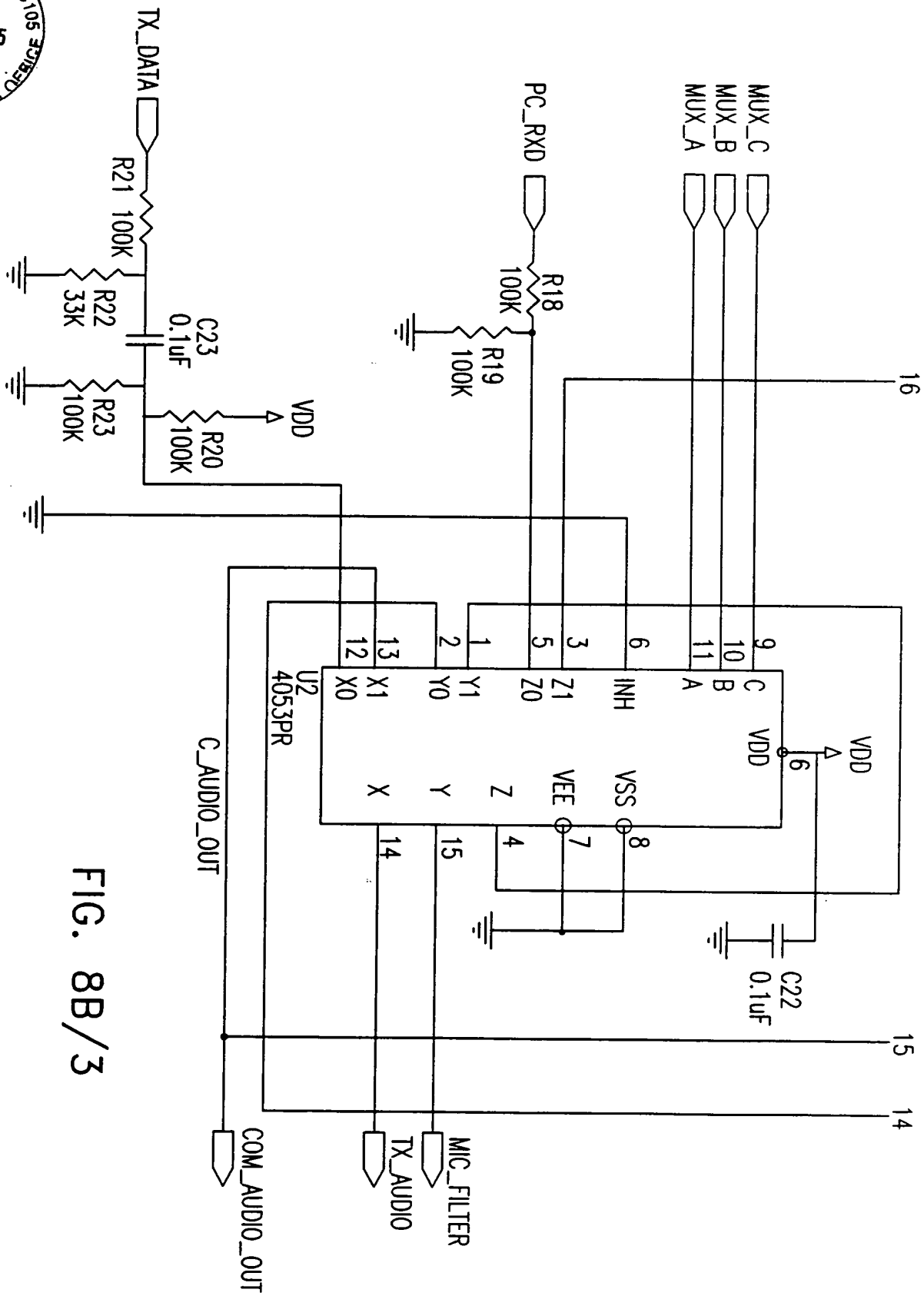


FIG. 8B/3



FIG. 8C/1

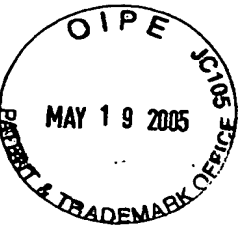
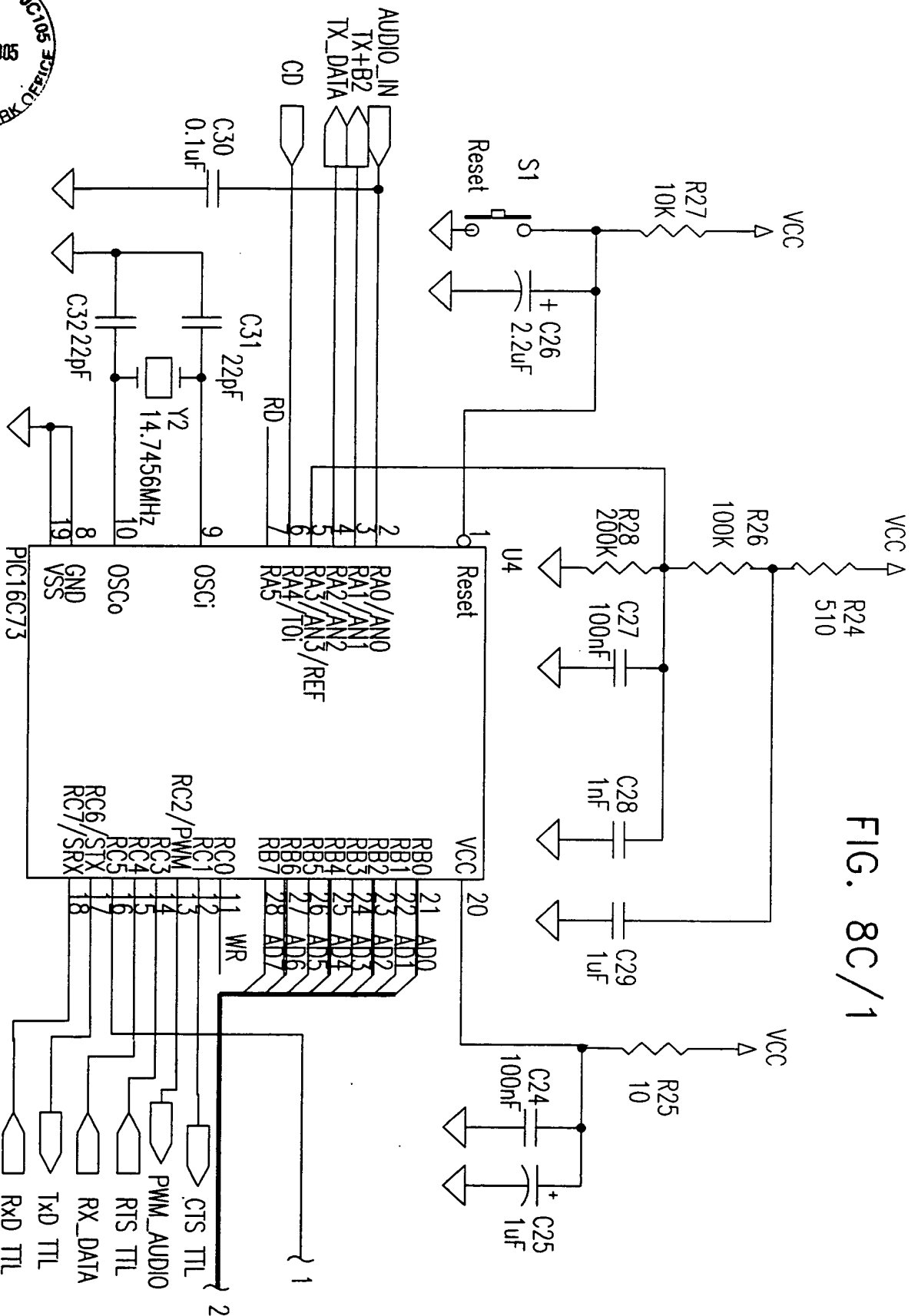


FIG. 8C/2

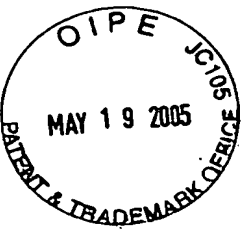
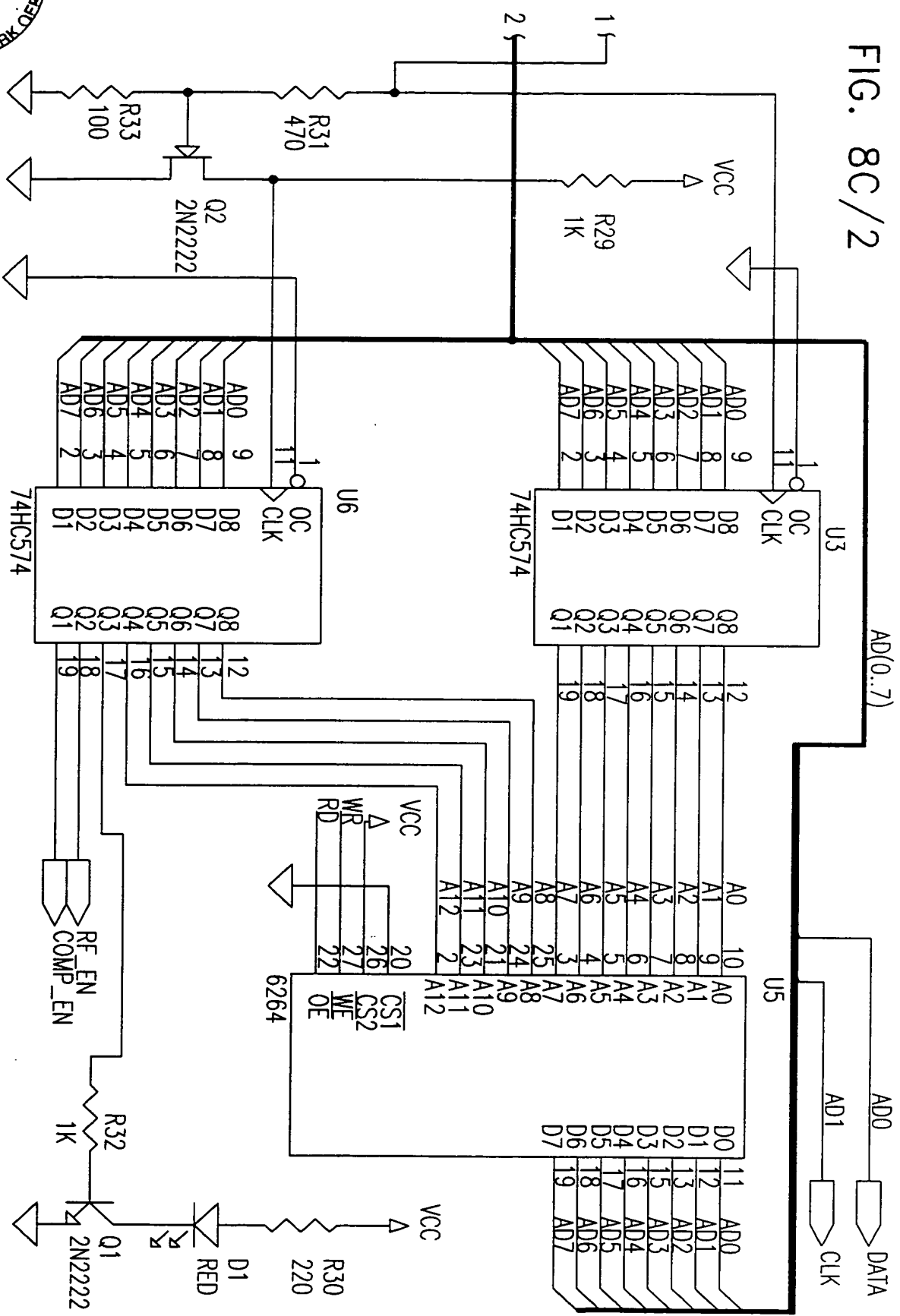


FIG. 8D/1

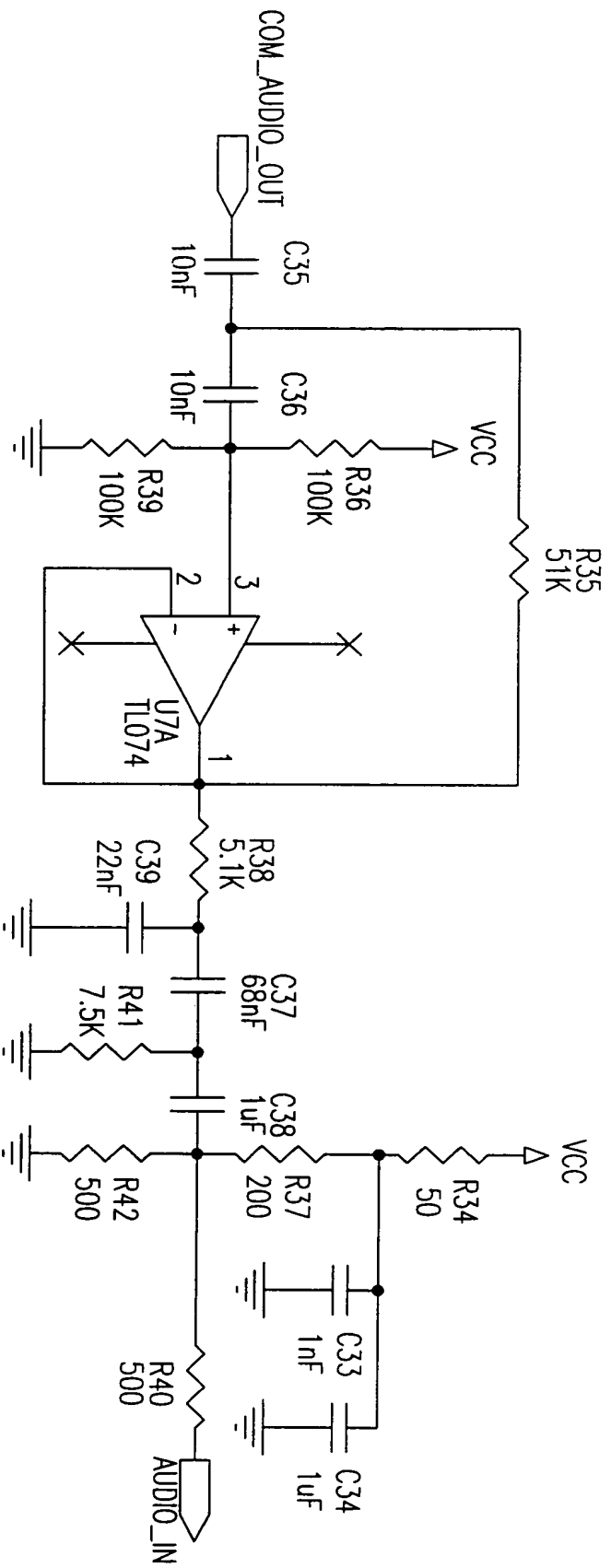




FIG. 8D/2

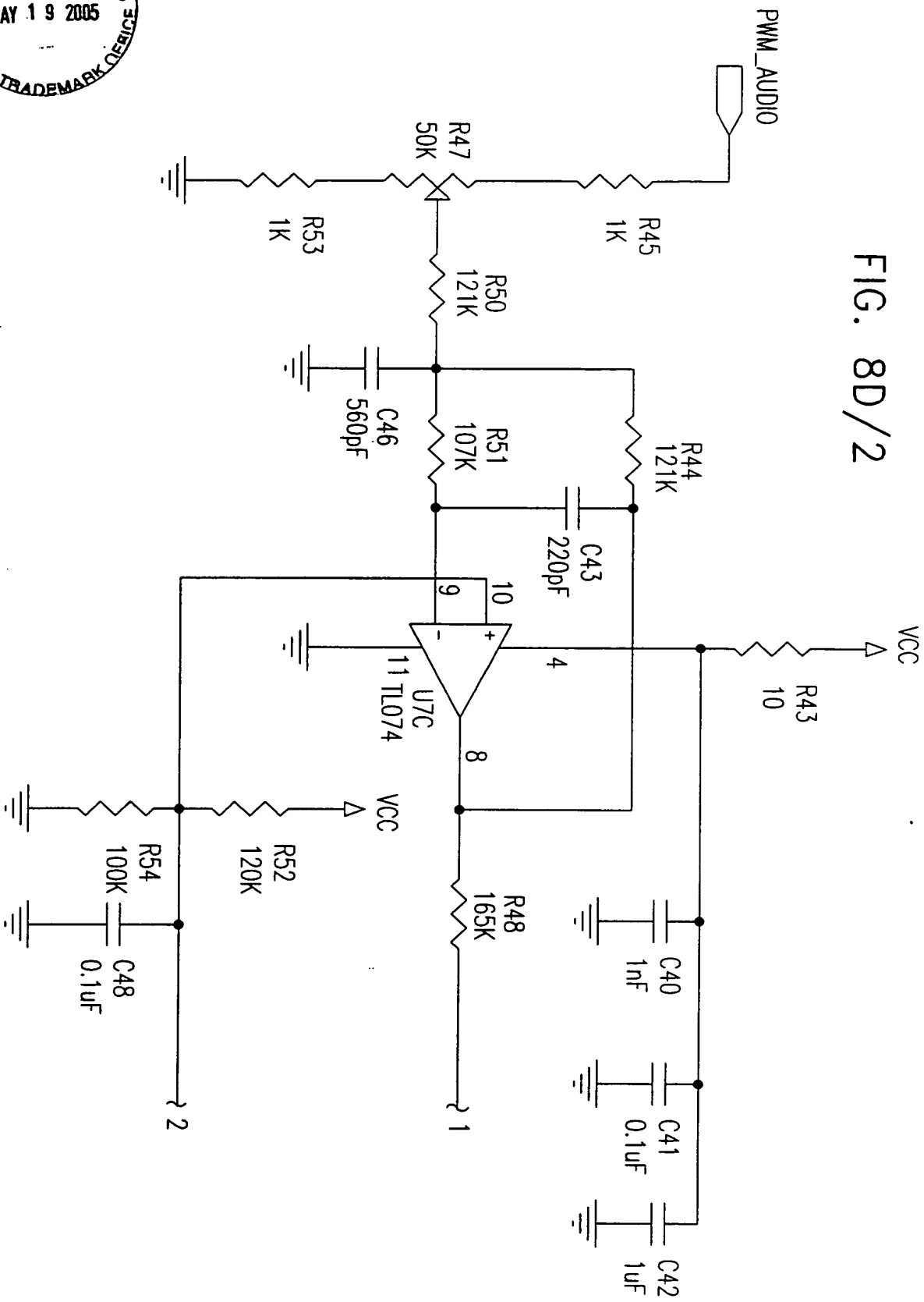


FIG. 8D/3

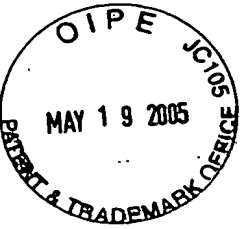
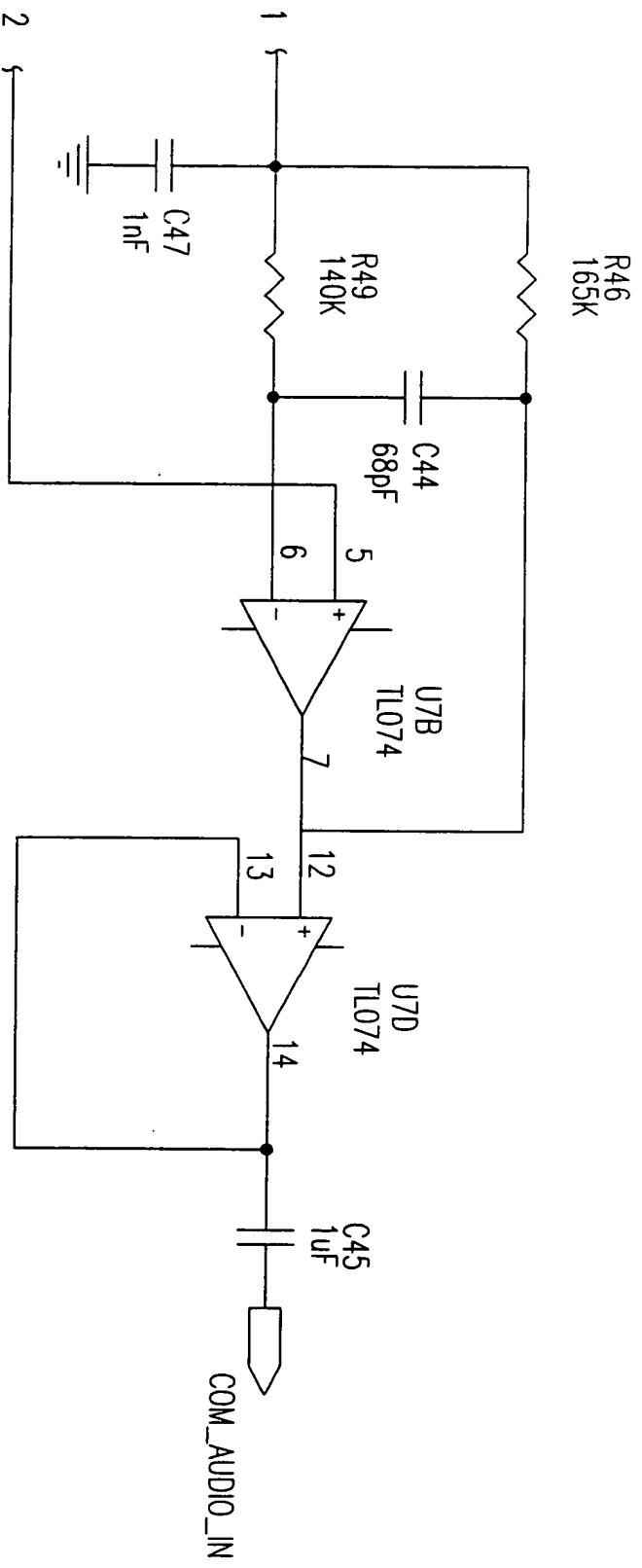


FIG. 8E

FIG. 8E is a schematic diagram of a circuit component, likely a microcontroller or IC, showing its pin connections. The component is labeled "CON28C" and has pins numbered 1 through 28. Pin 1 is connected to a "JUMPER" and a "JP1" (jump point). Pin 2 is connected to a "JUMPER" and a "JP2" (jump point). Pin 3 is connected to a "JUMPER". Pin 4 is connected to a "JUMPER". Pin 5 is connected to a "JUMPER". Pin 6 is connected to a "JUMPER". Pin 7 is connected to a "JUMPER". Pin 8 is connected to a "JUMPER". Pin 9 is connected to a "JUMPER". Pin 10 is connected to a "JUMPER". Pin 11 is connected to a "JUMPER". Pin 12 is connected to a "JUMPER". Pin 13 is connected to a "JUMPER". Pin 14 is connected to a "JUMPER". Pin 15 is connected to a "JUMPER". Pin 16 is connected to a "JUMPER". Pin 17 is connected to a "JUMPER". Pin 18 is connected to a "JUMPER". Pin 19 is connected to a "JUMPER". Pin 20 is connected to a "JUMPER". Pin 21 is connected to a "JUMPER". Pin 22 is connected to a "JUMPER". Pin 23 is connected to a "JUMPER". Pin 24 is connected to a "JUMPER". Pin 25 is connected to a "JUMPER". Pin 26 is connected to a "JUMPER". Pin 27 is connected to a "JUMPER". Pin 28 is connected to a "JUMPER". The component is also connected to a "VCC" supply and a "100uF" capacitor (C49). The component is labeled "CON28C".

FIG. 8F

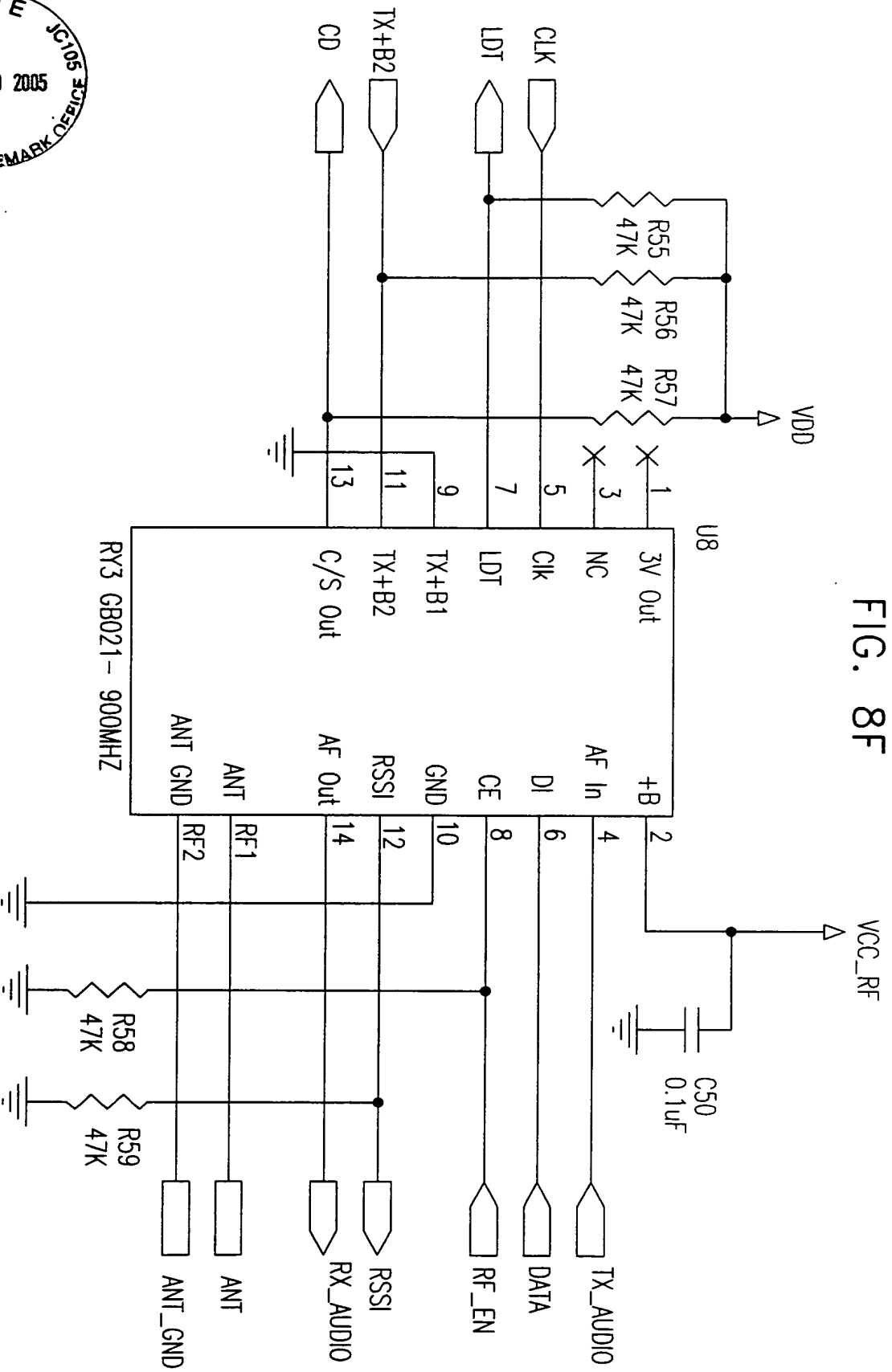
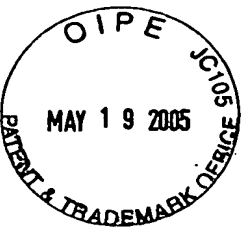
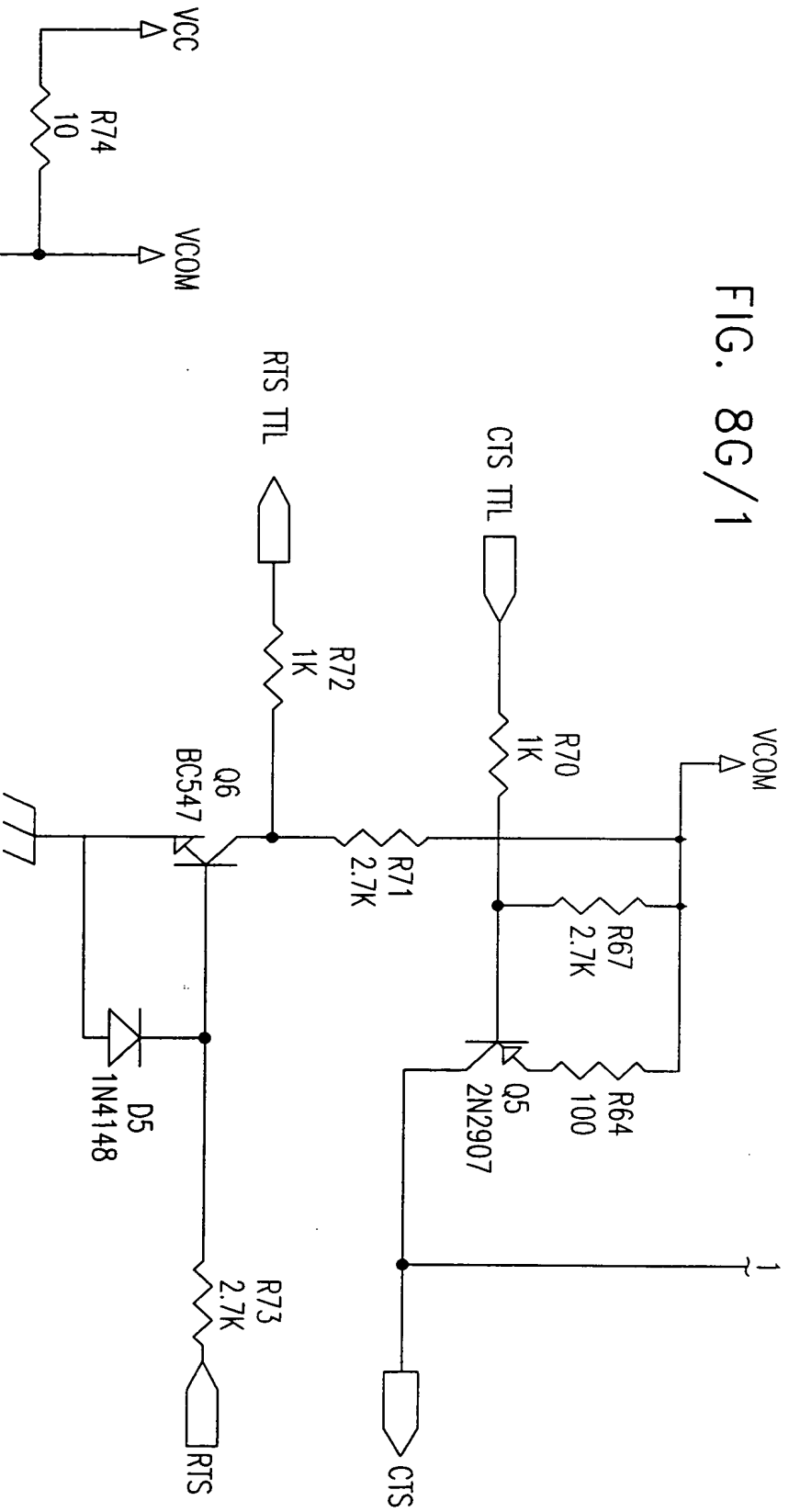


FIG. 8C/1



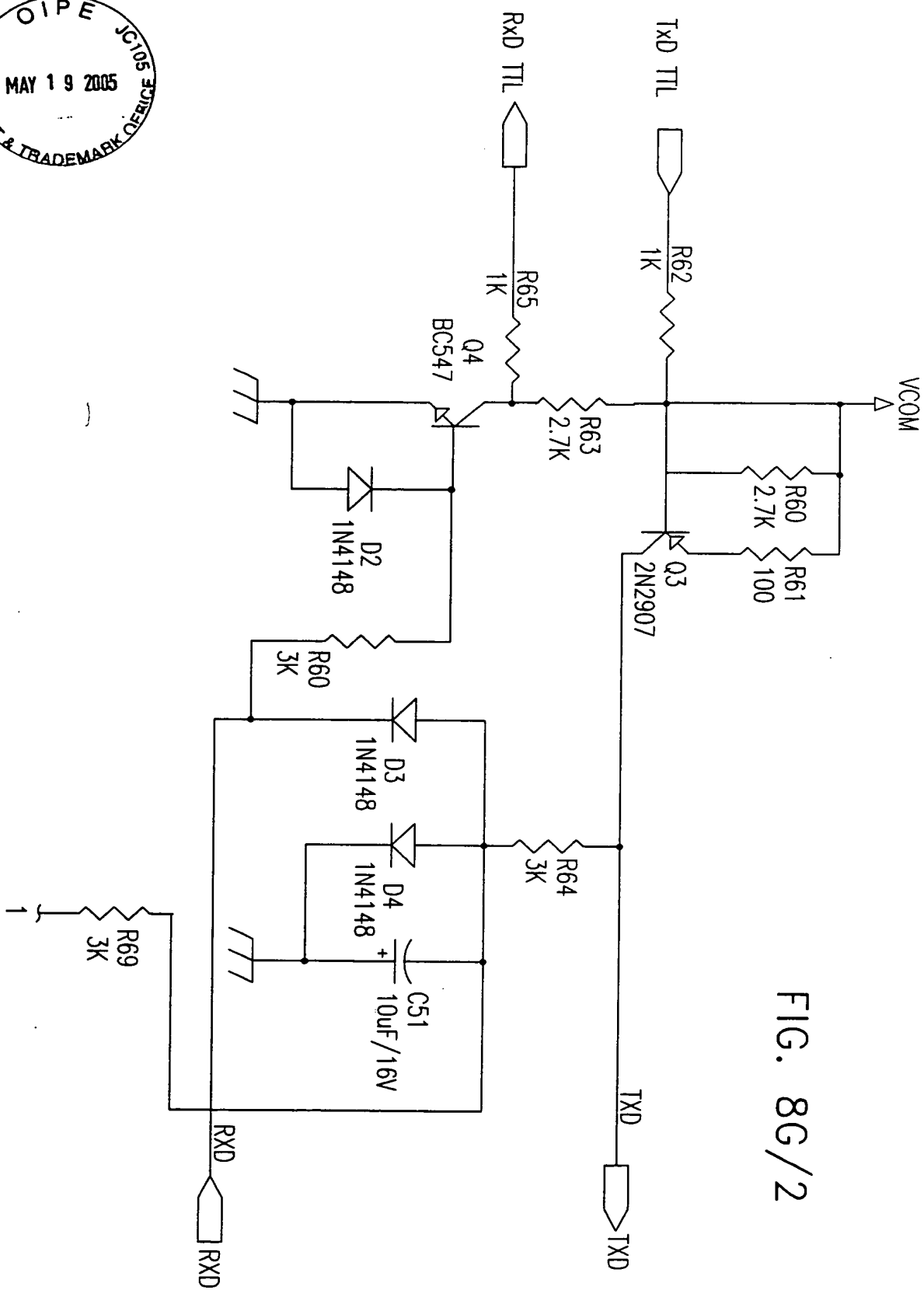
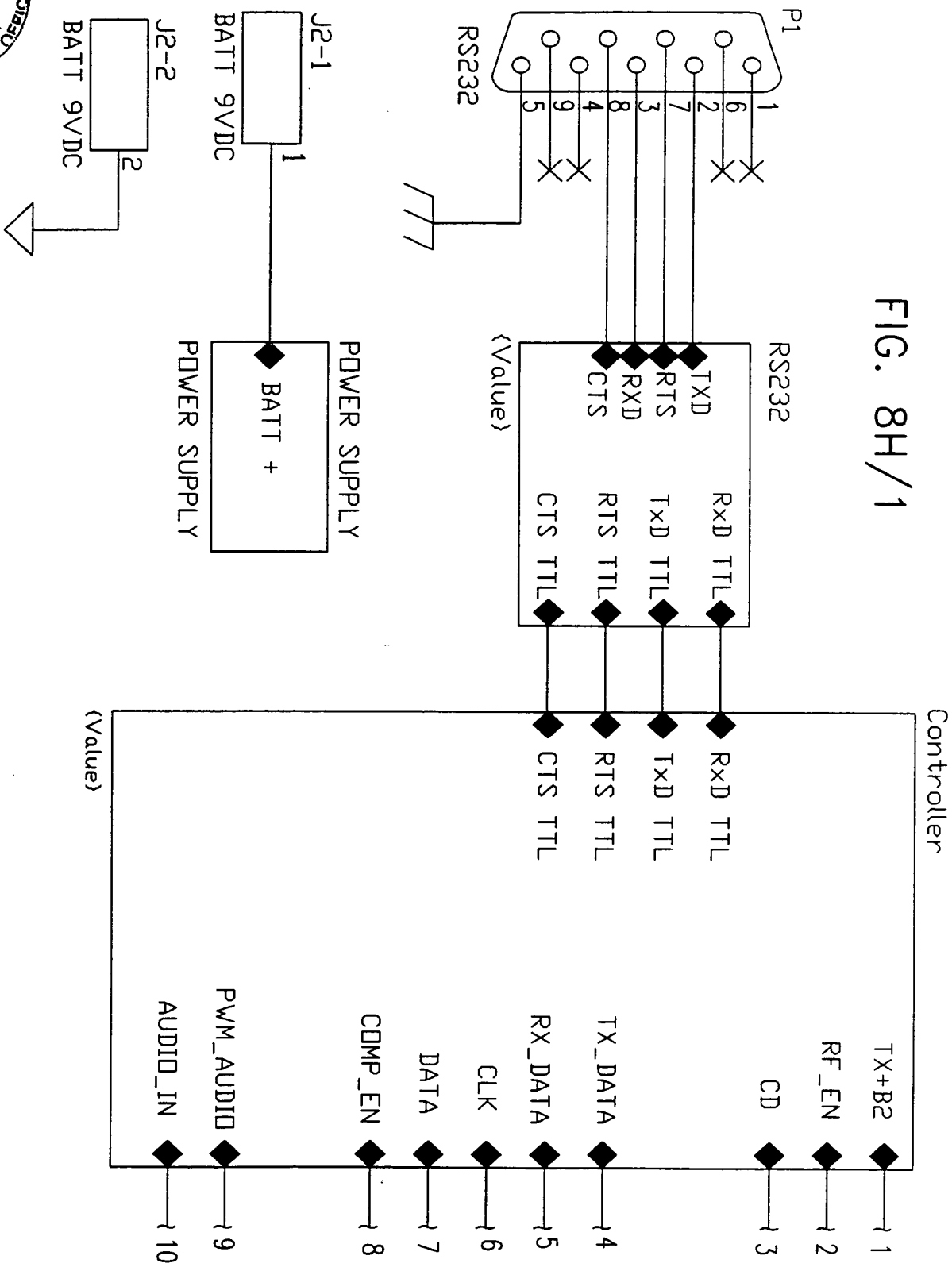


FIG. 8H/1



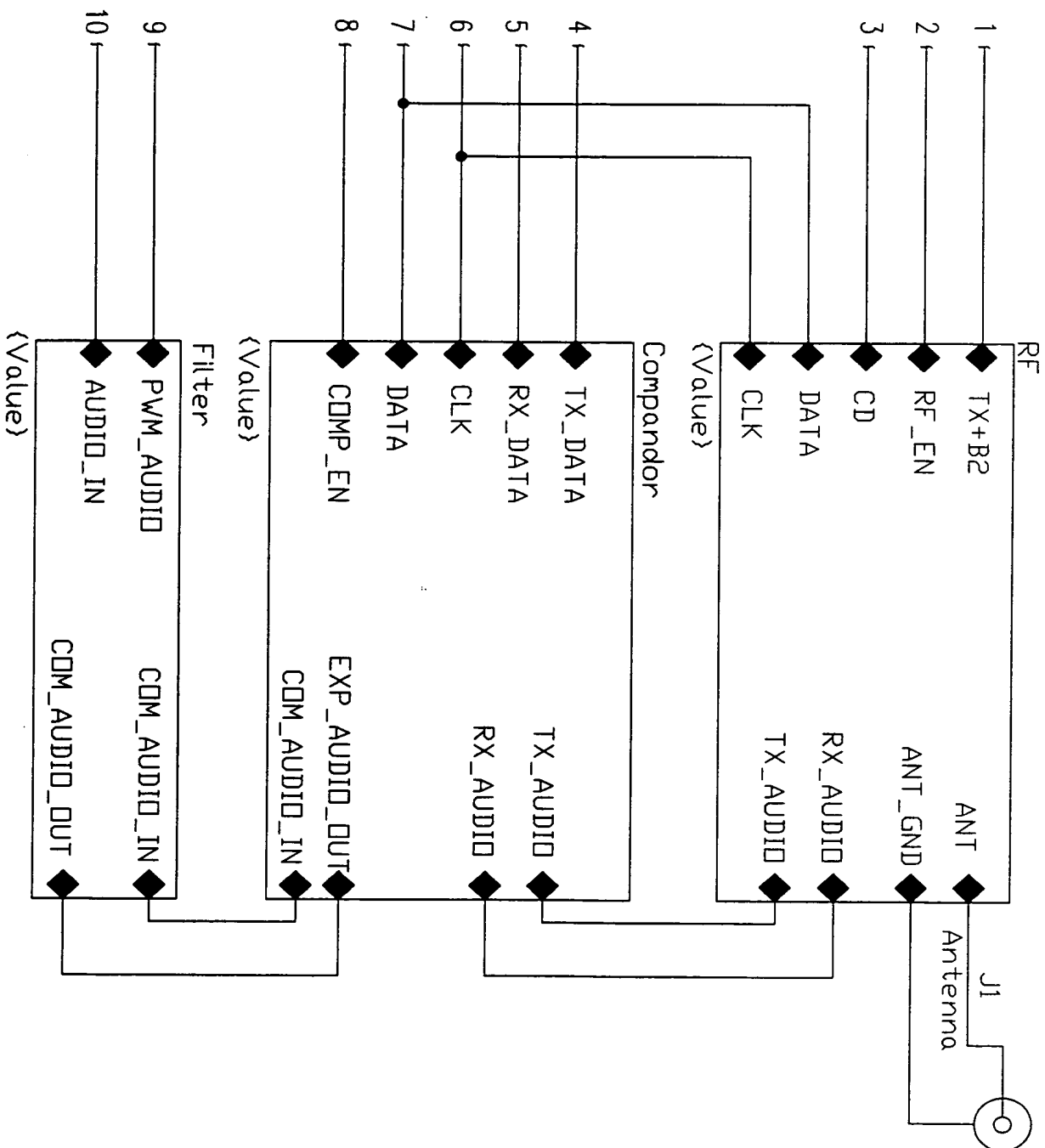


FIG. 8H/2

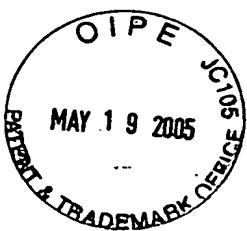
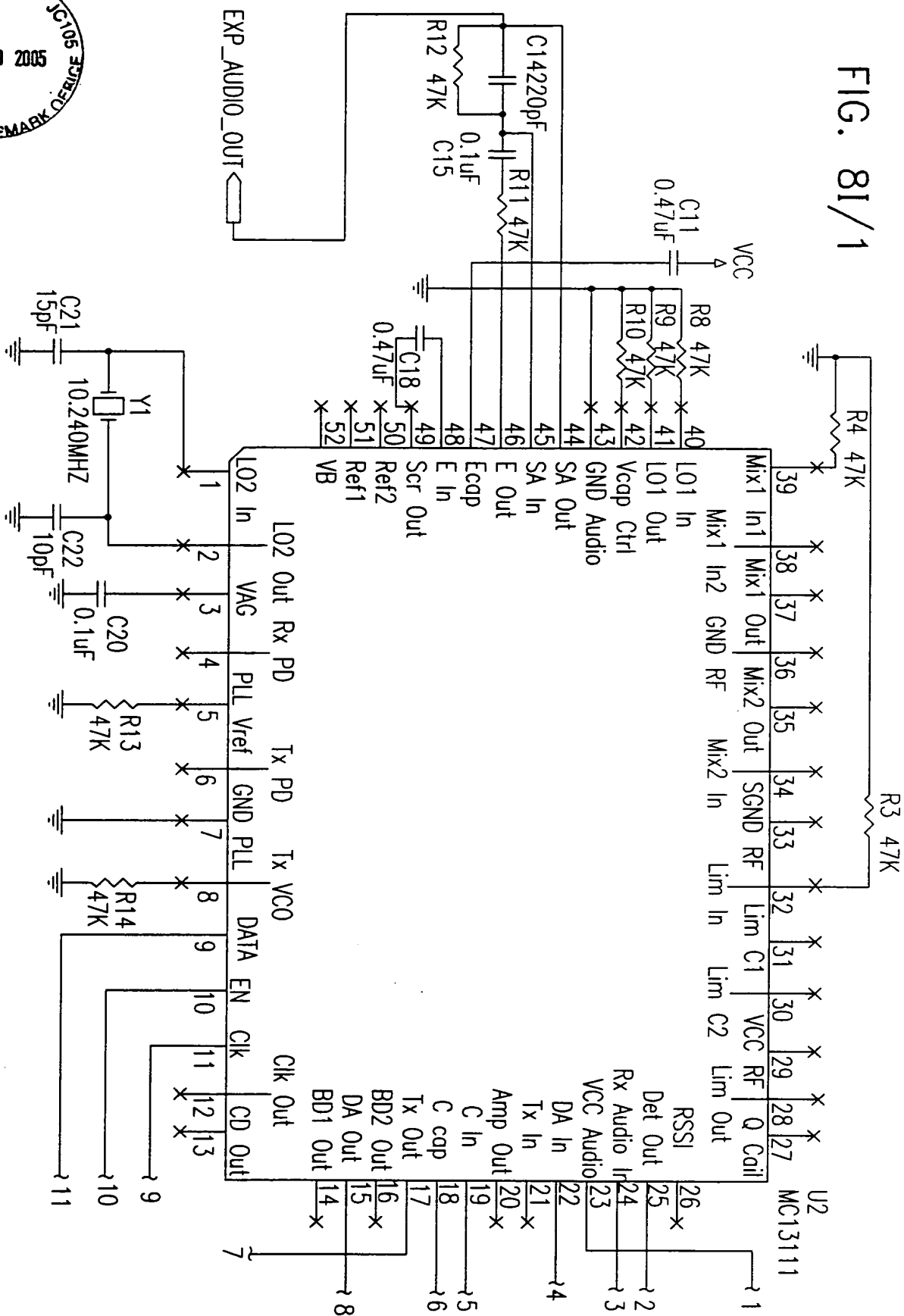




FIG. 81/1



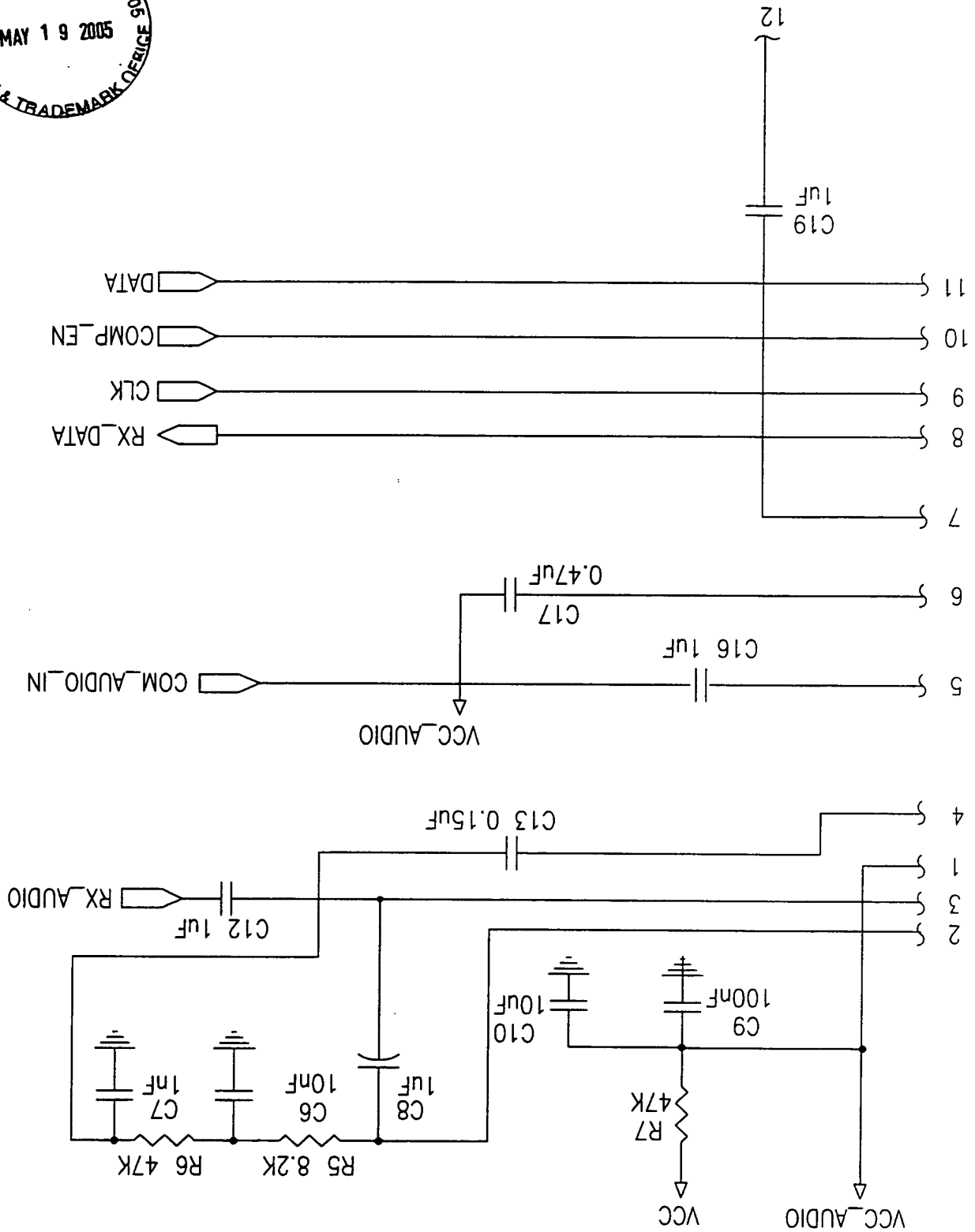


FIG. 81/2

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FIG. 81/3

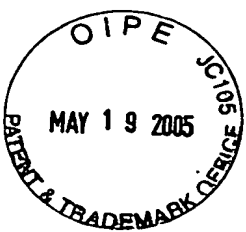
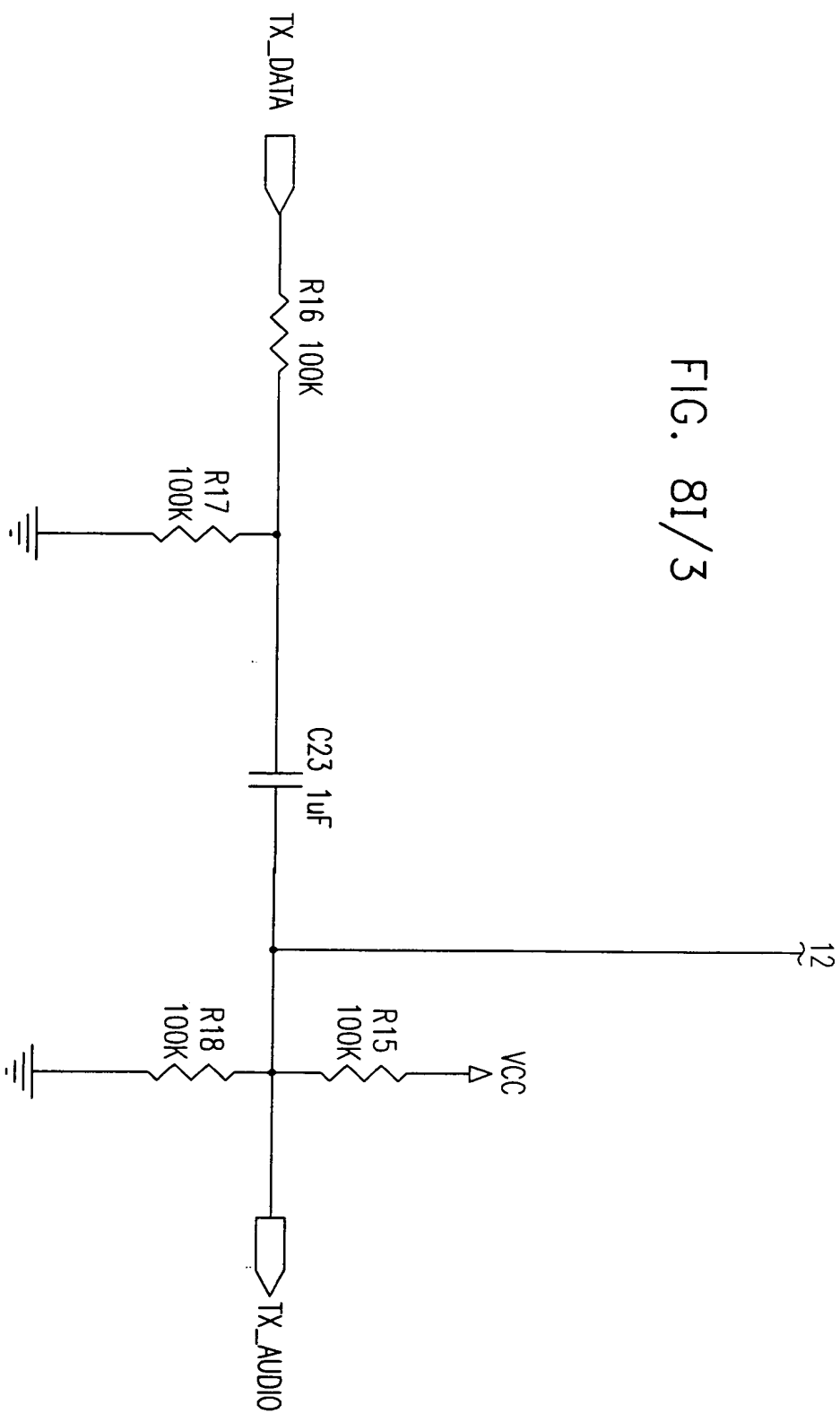


FIG. 8J/1

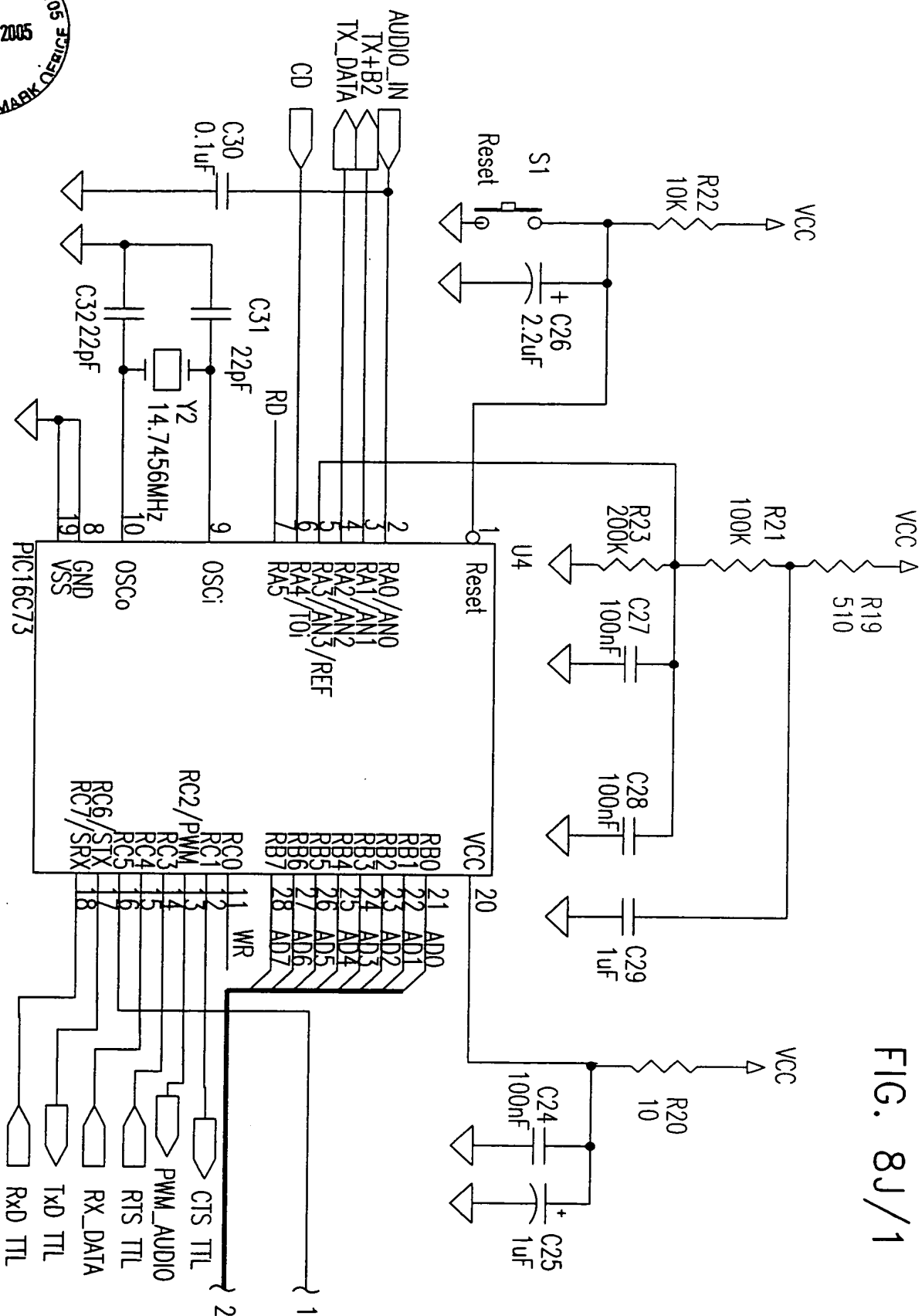


FIG. 8J/2

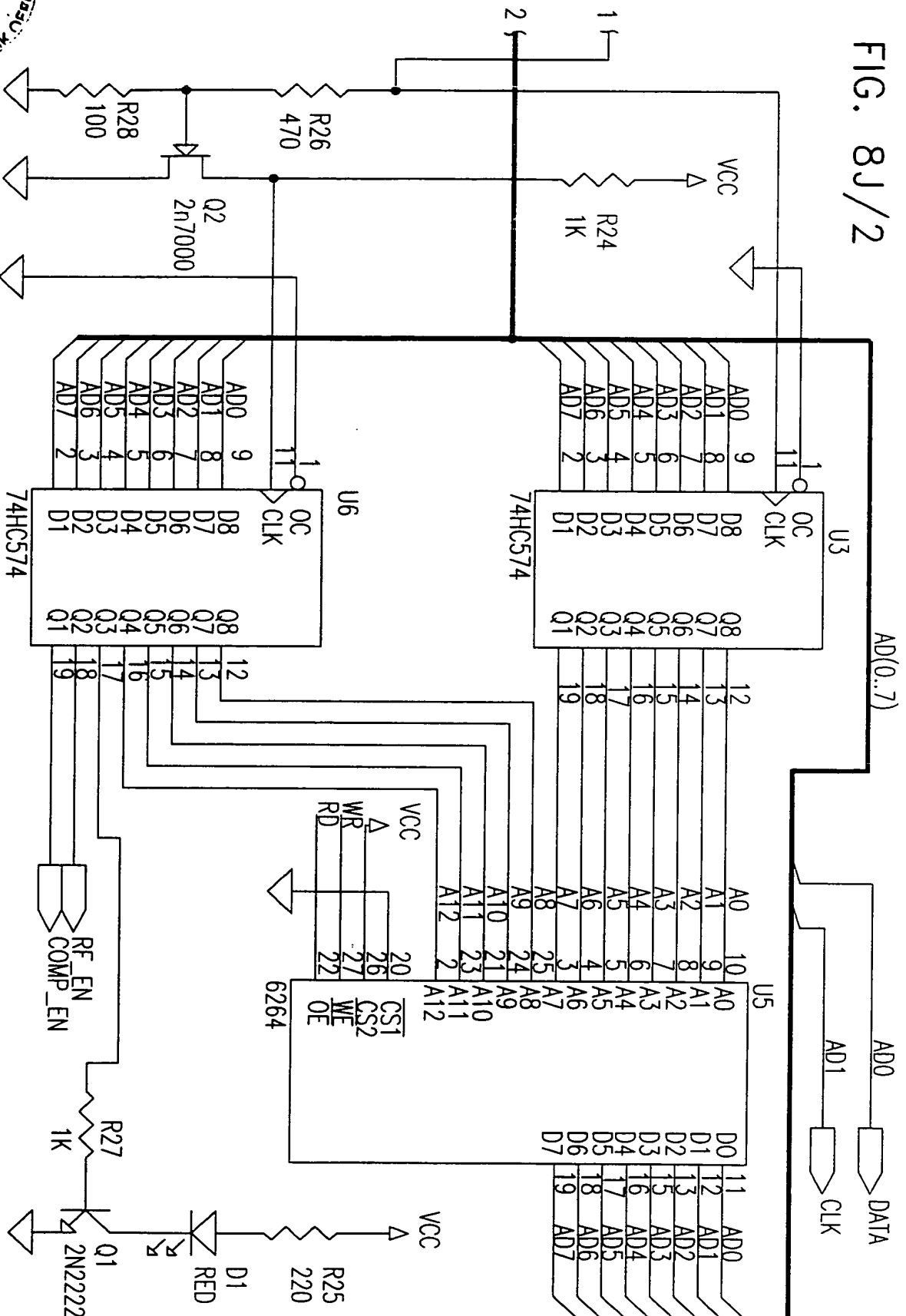


FIG. 8K/1

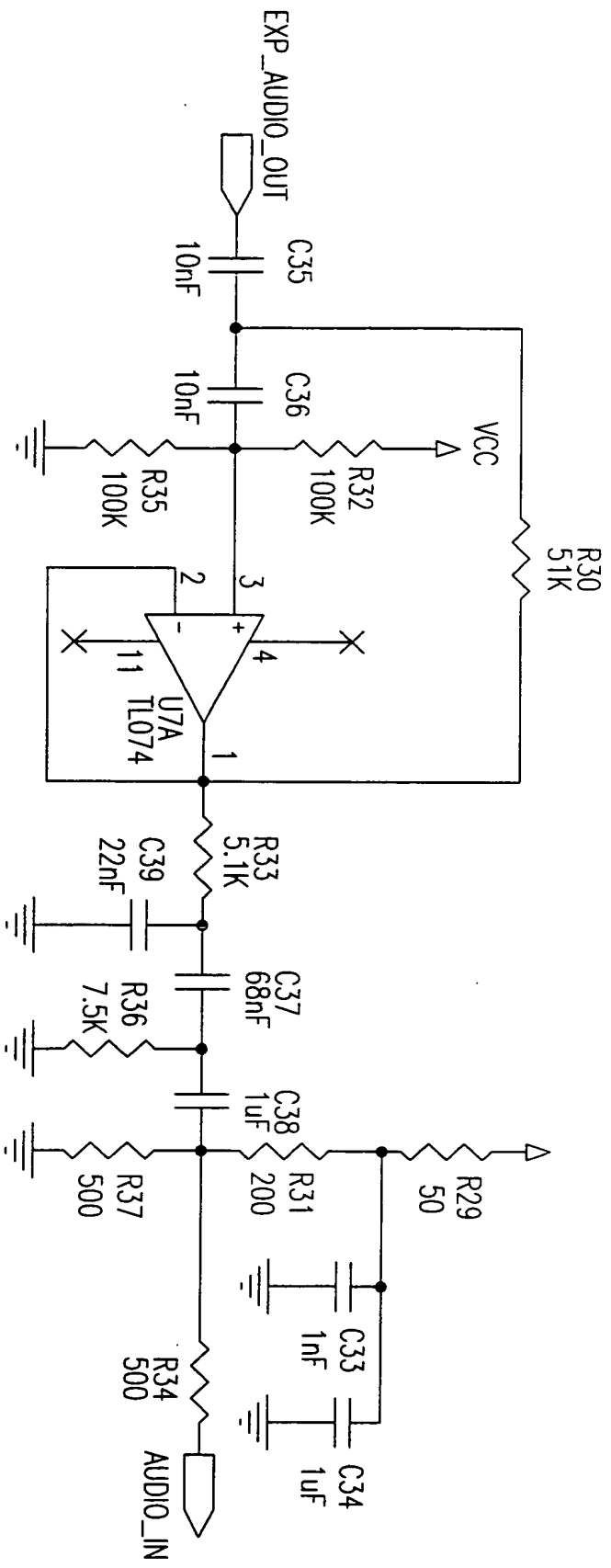


FIG. 8K/2

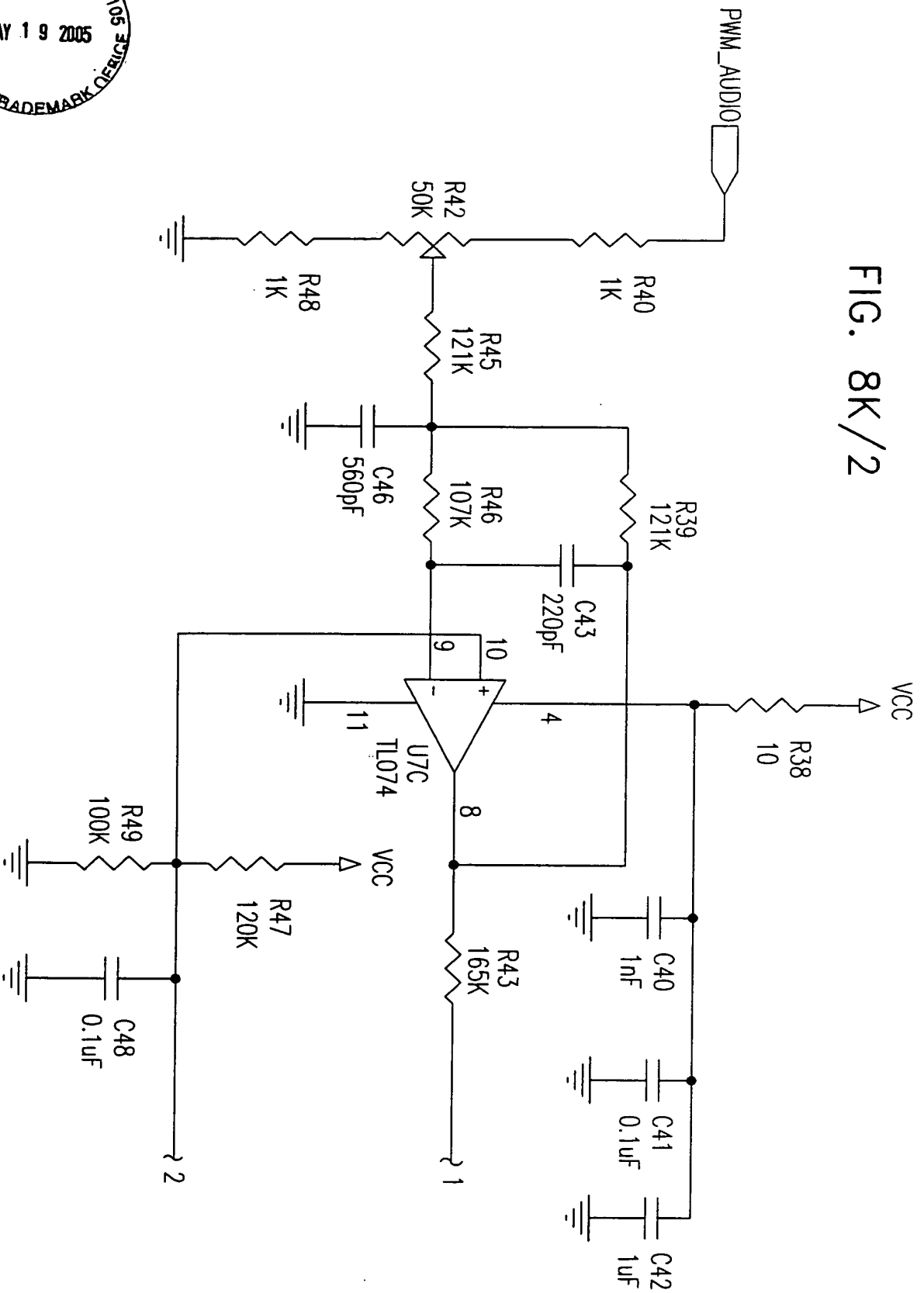


FIG. 8K/3

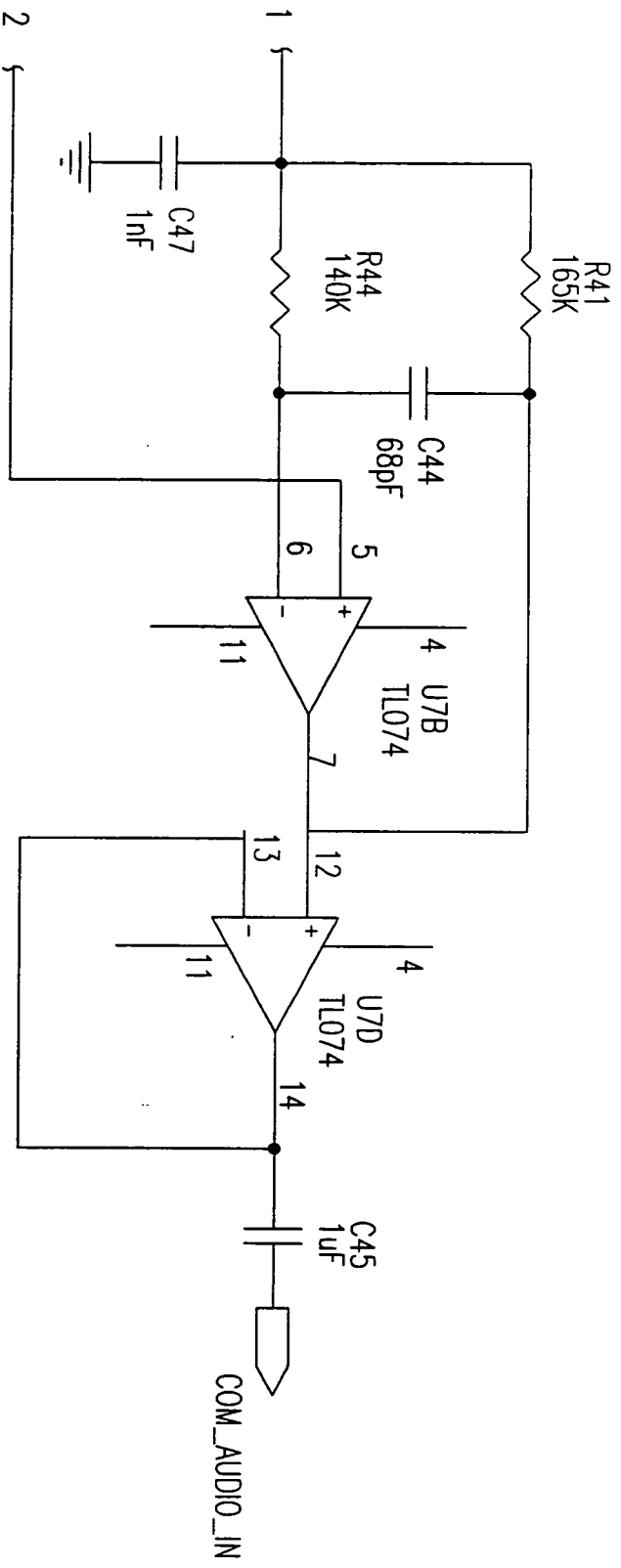




FIG. 8L

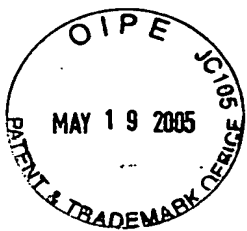
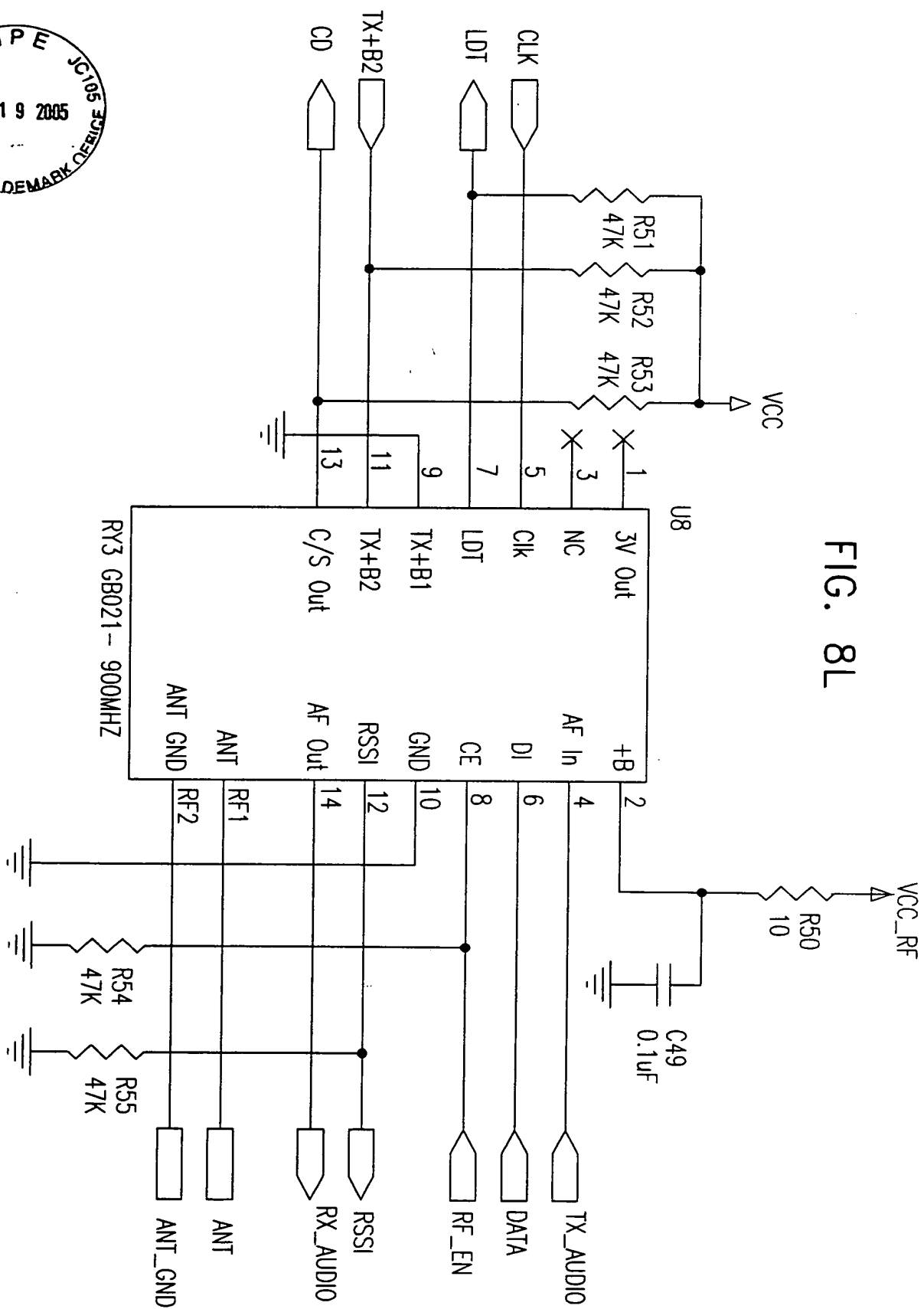


FIG. 8M

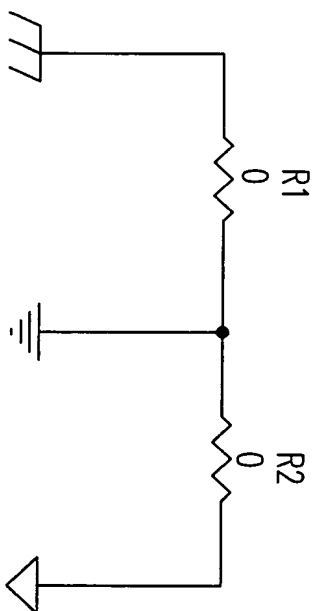
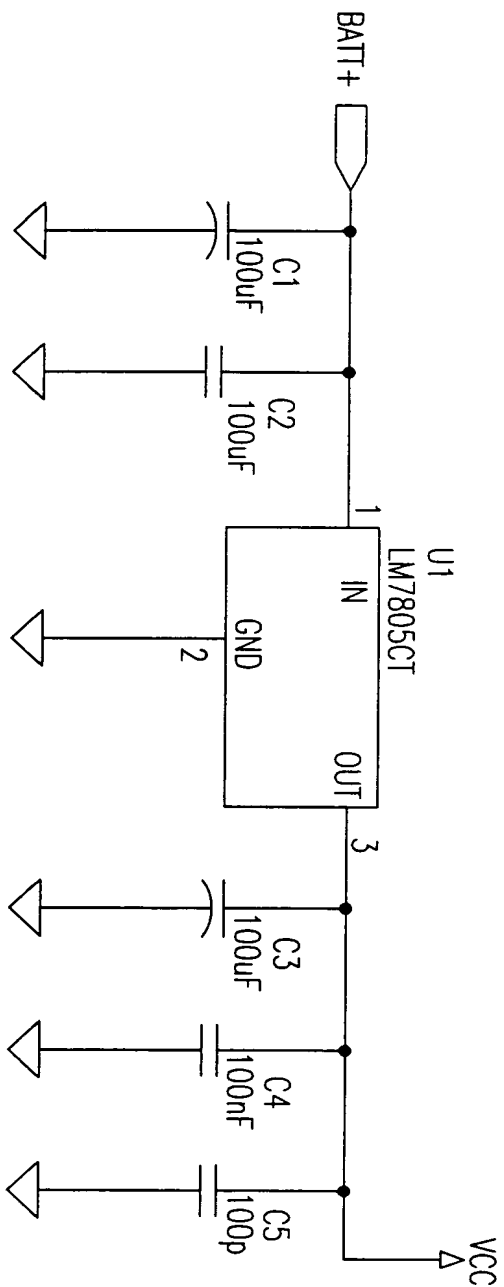


FIG. 8N/1

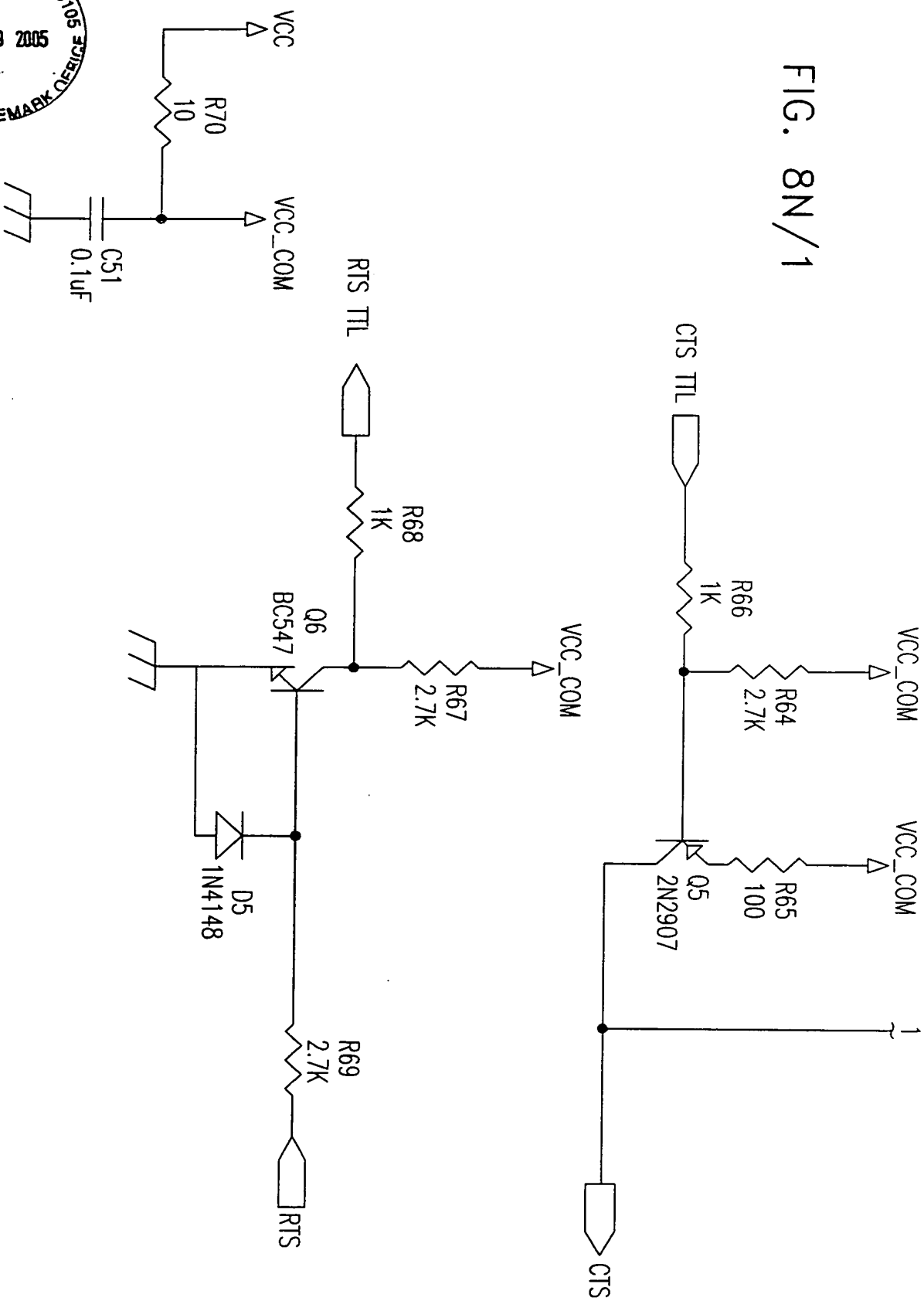




FIG. 9A/1

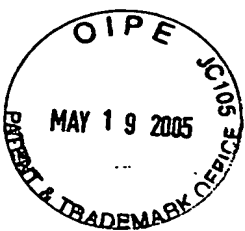
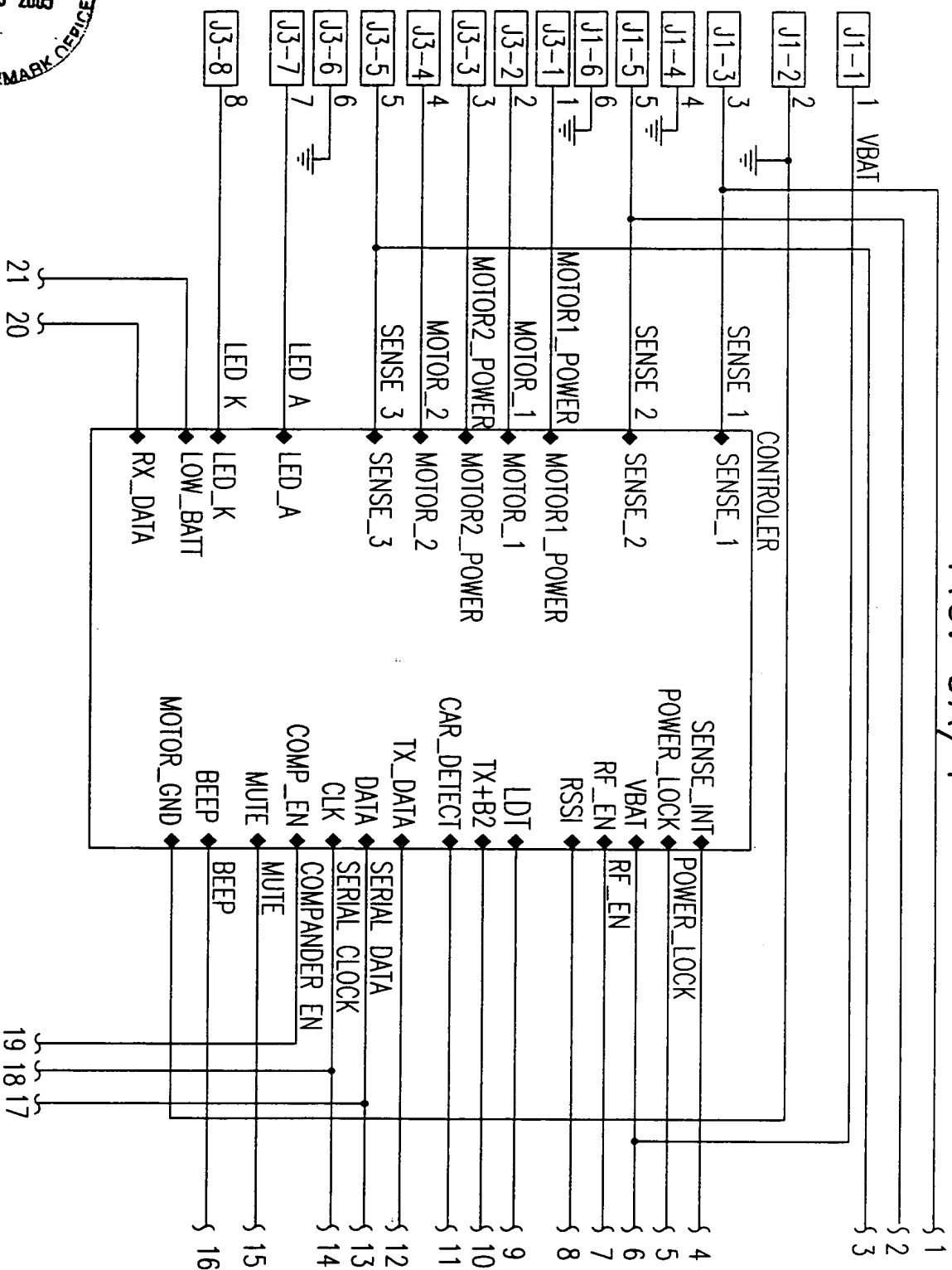


FIG. 9A/2

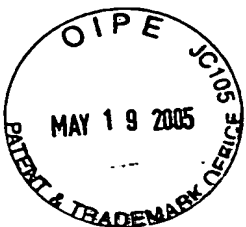
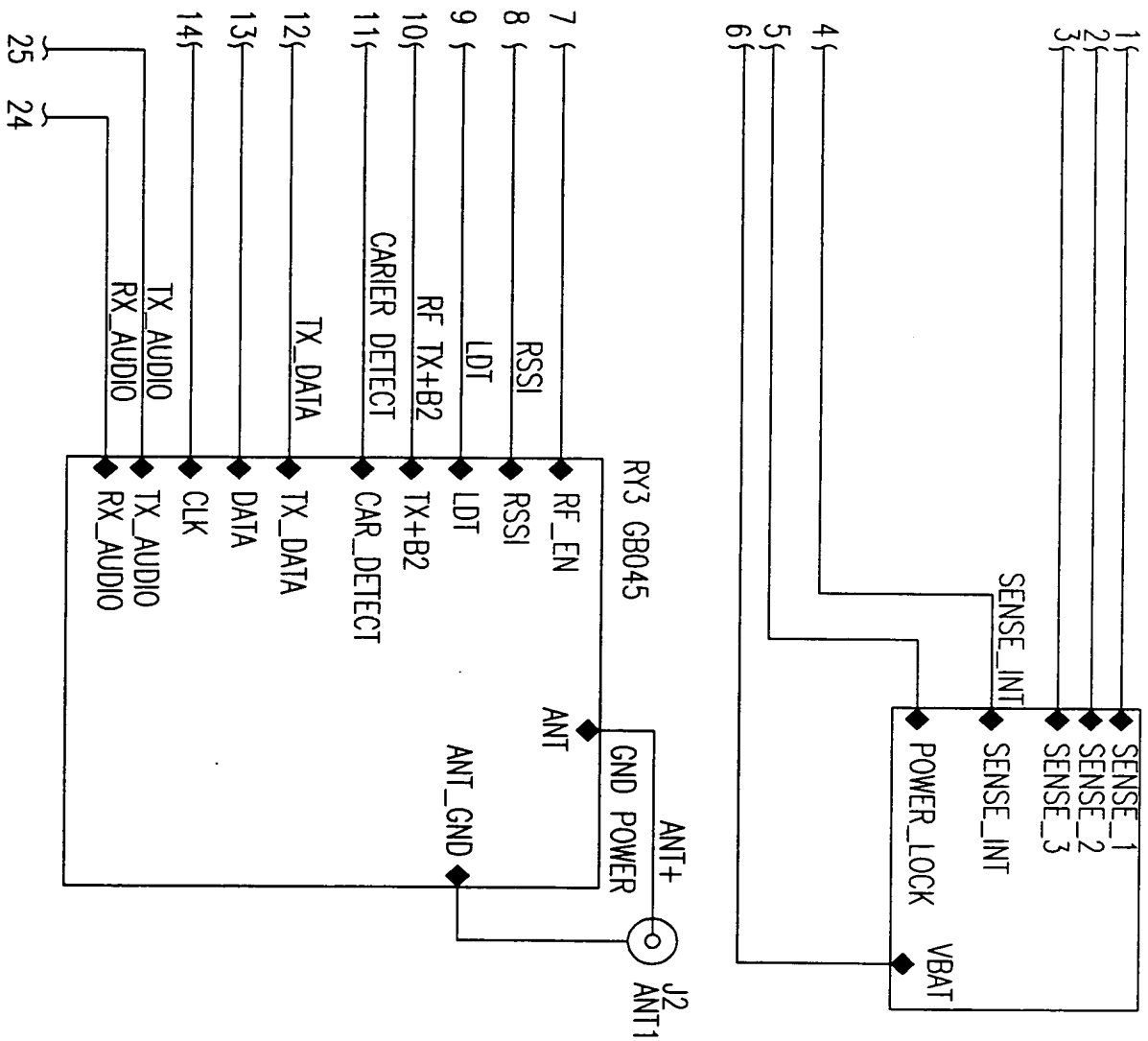


FIG. 9A/3

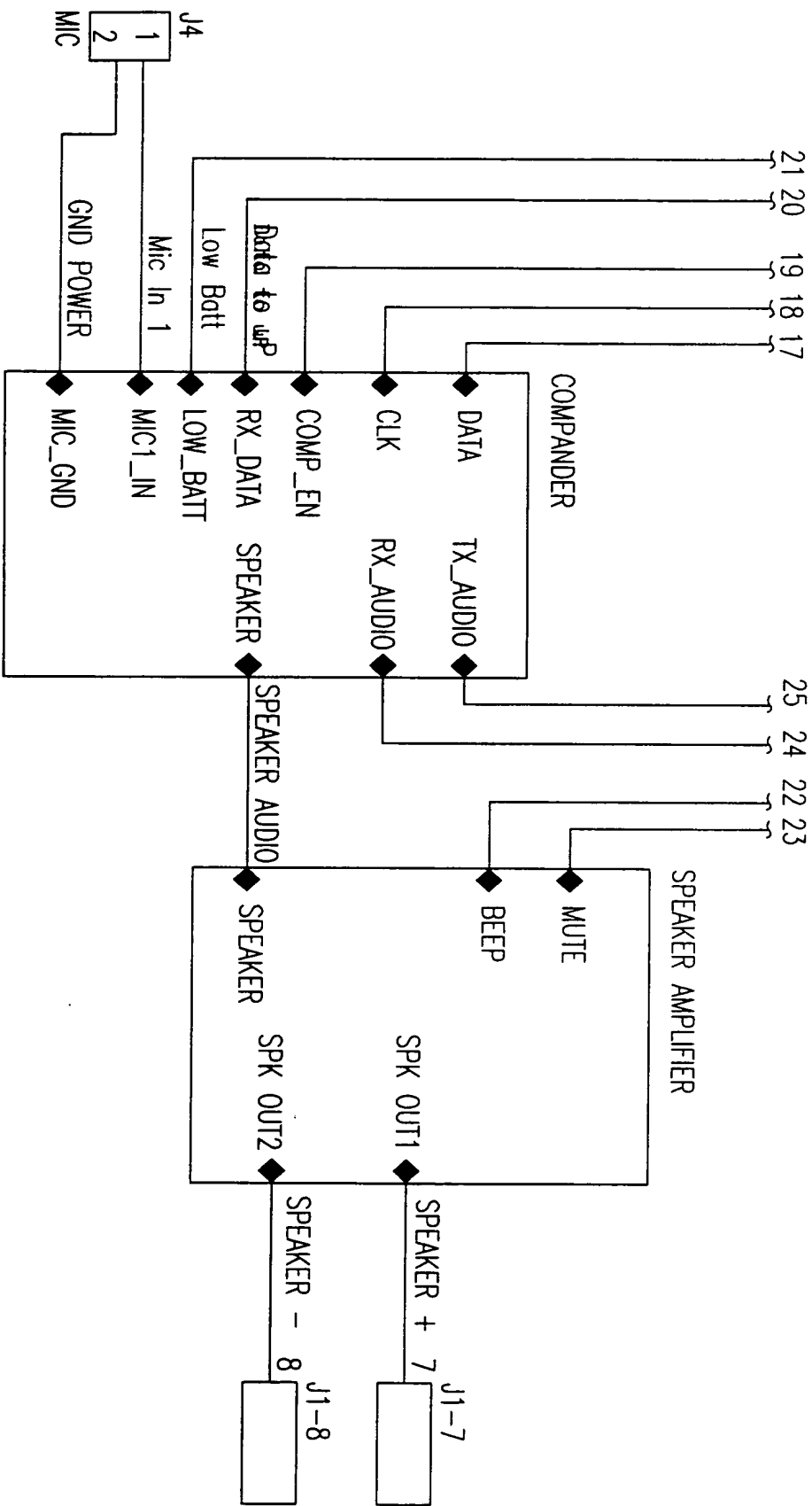


FIG. 9B/1

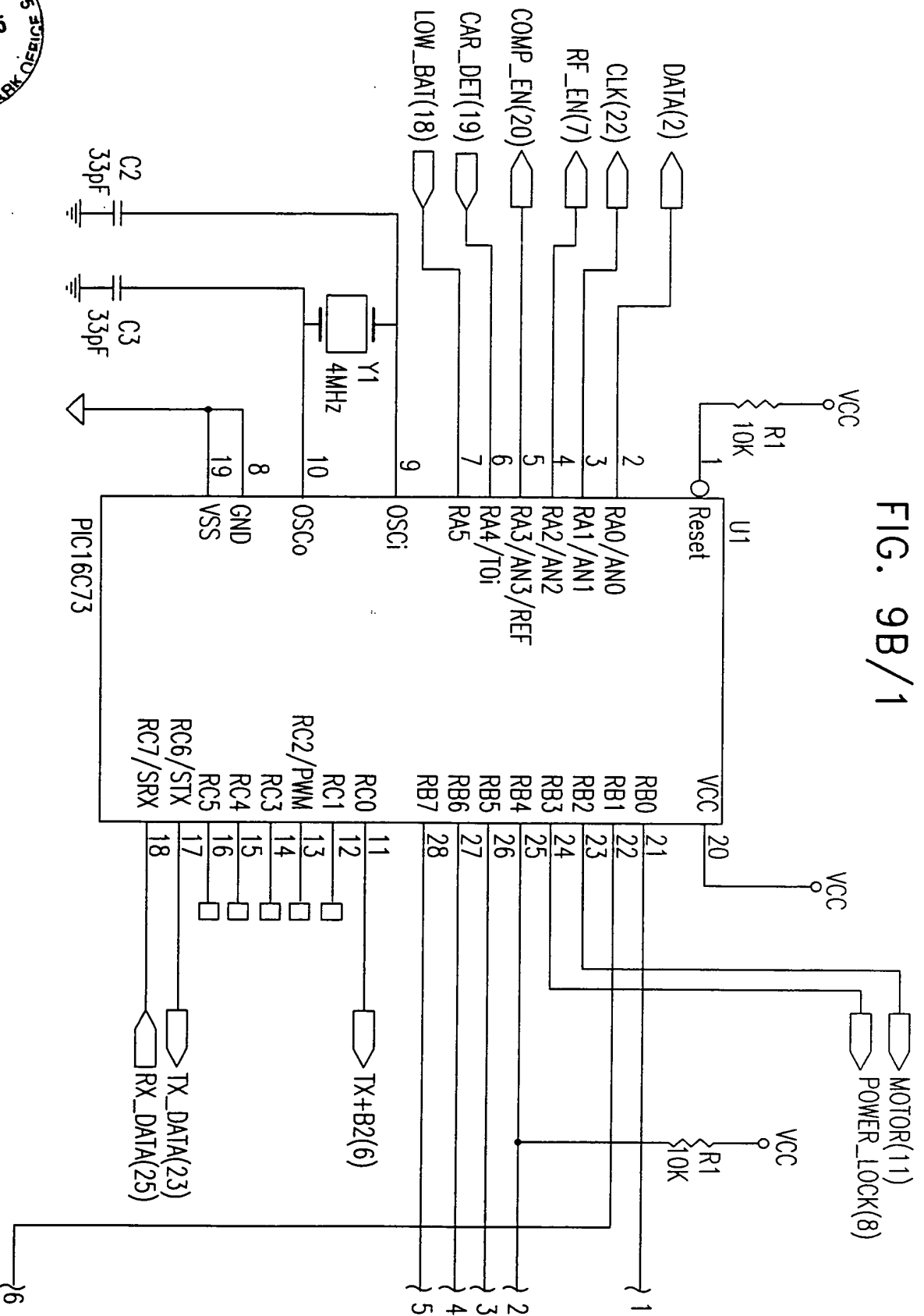
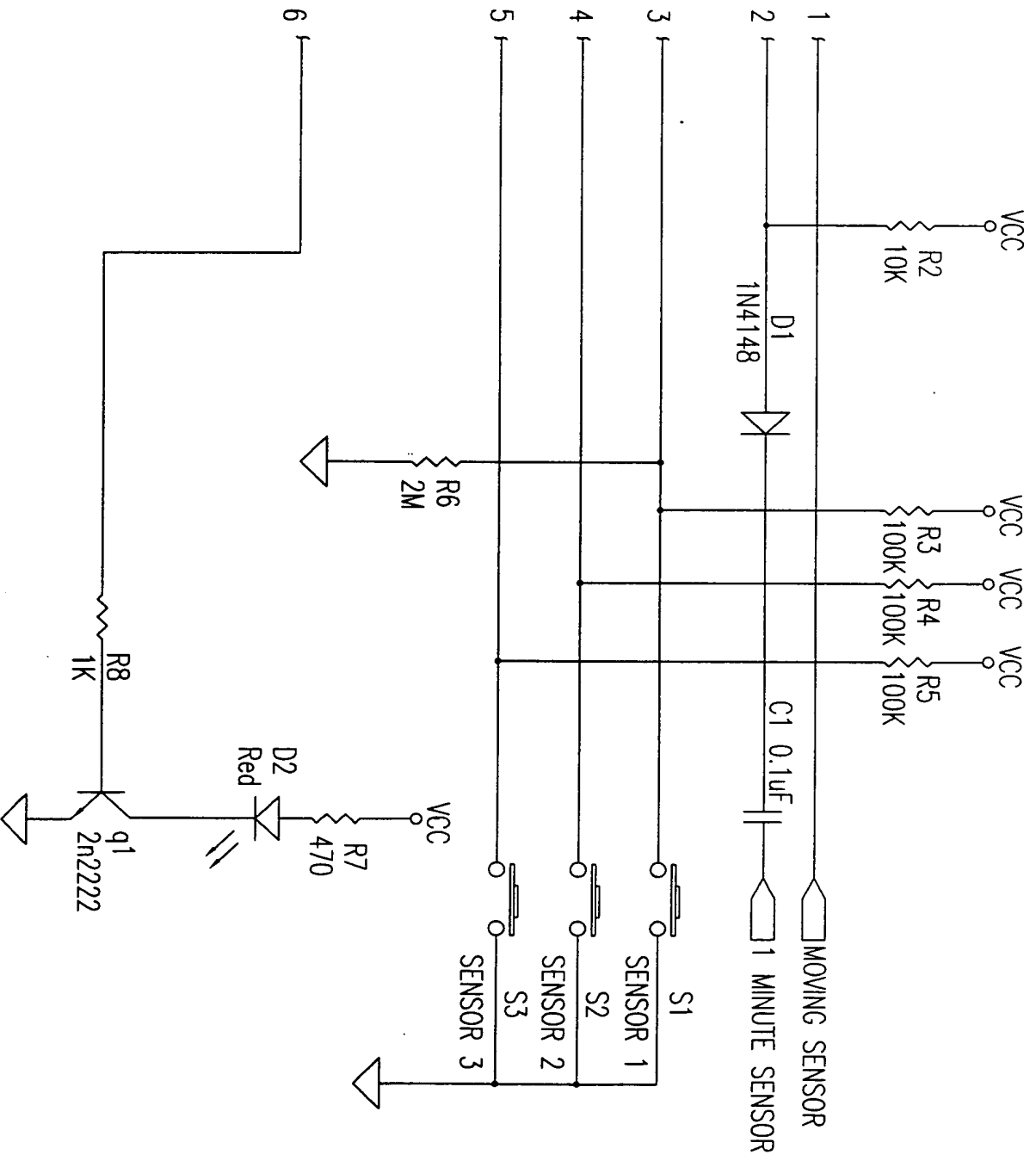




FIG. 9B/2



[illegible]

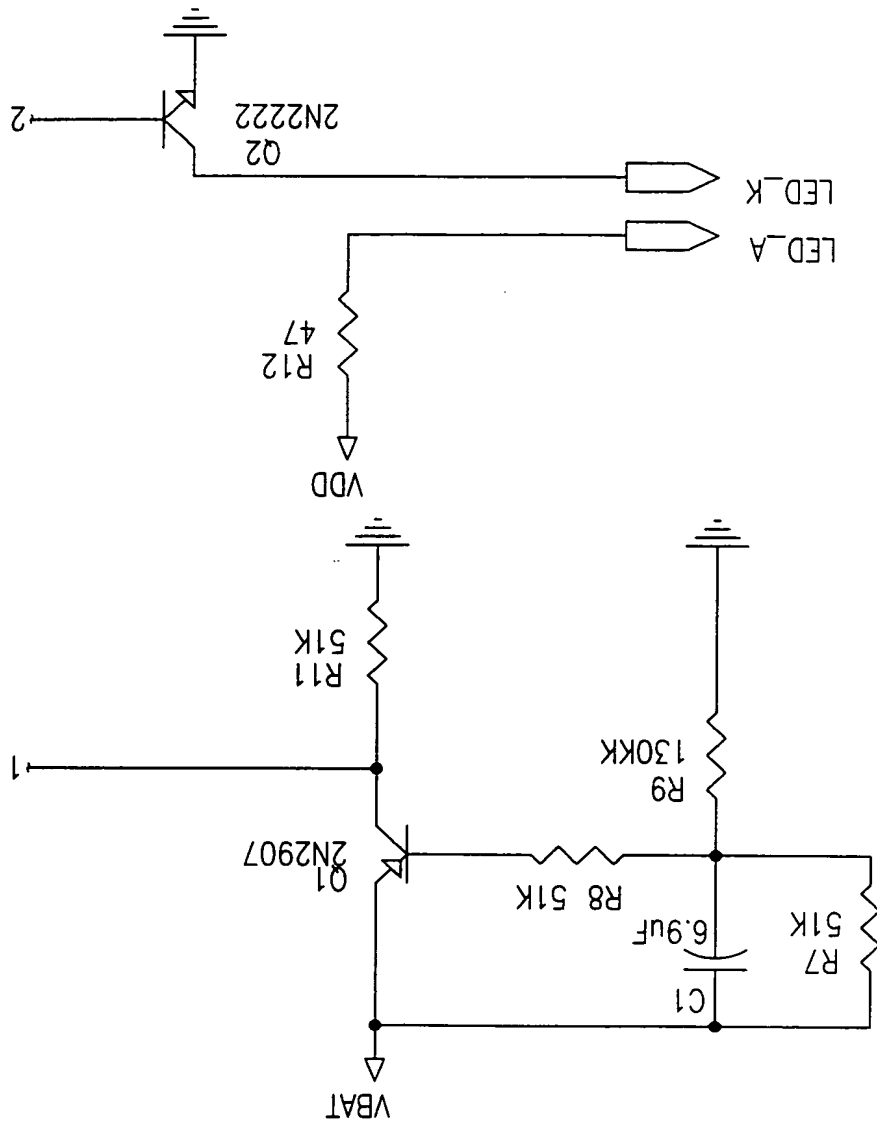
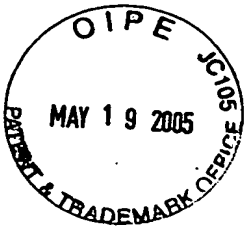


FIG. 9C/2

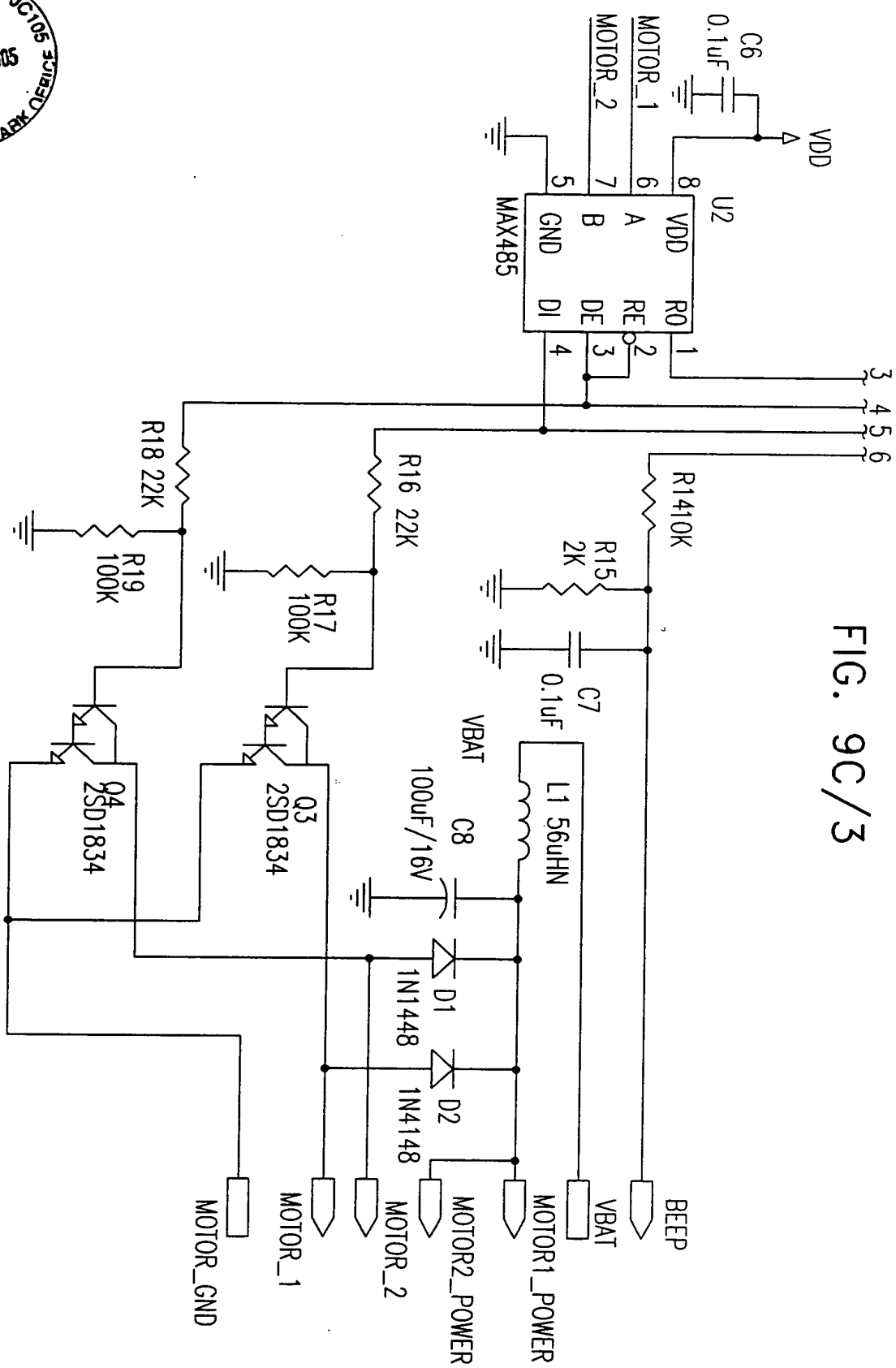
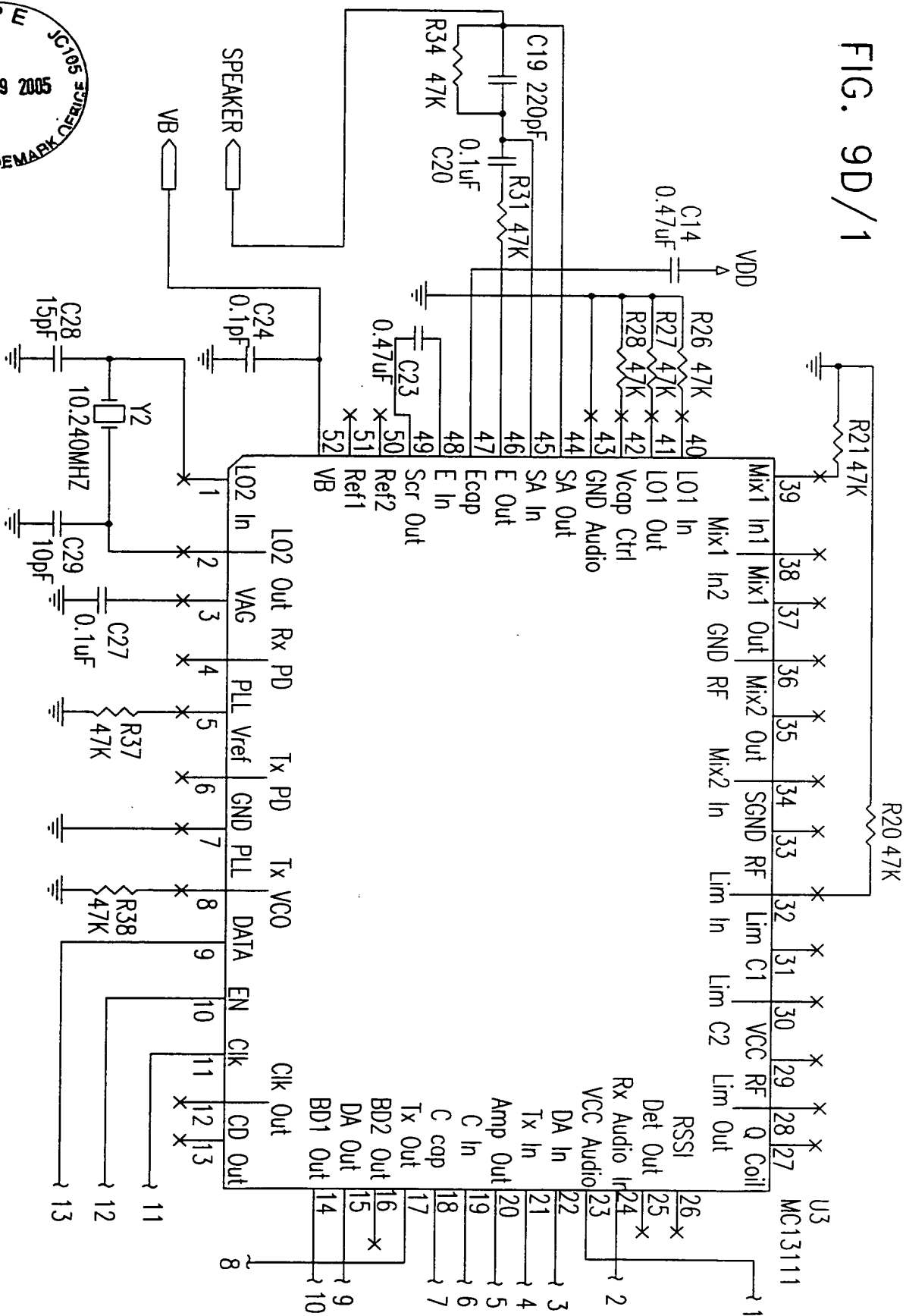
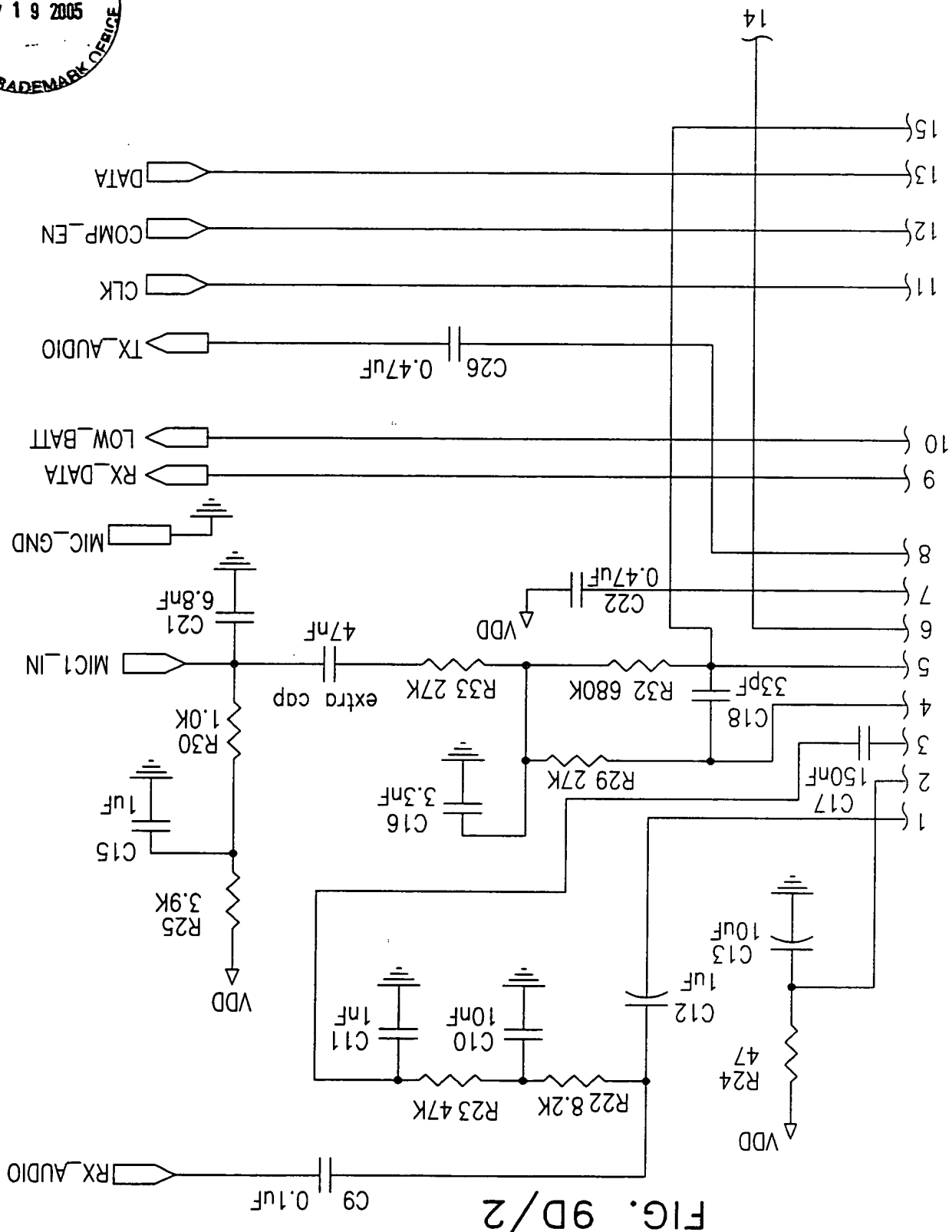


FIG. 9D/1





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FIG. 9D/2

FIG. 9D/3

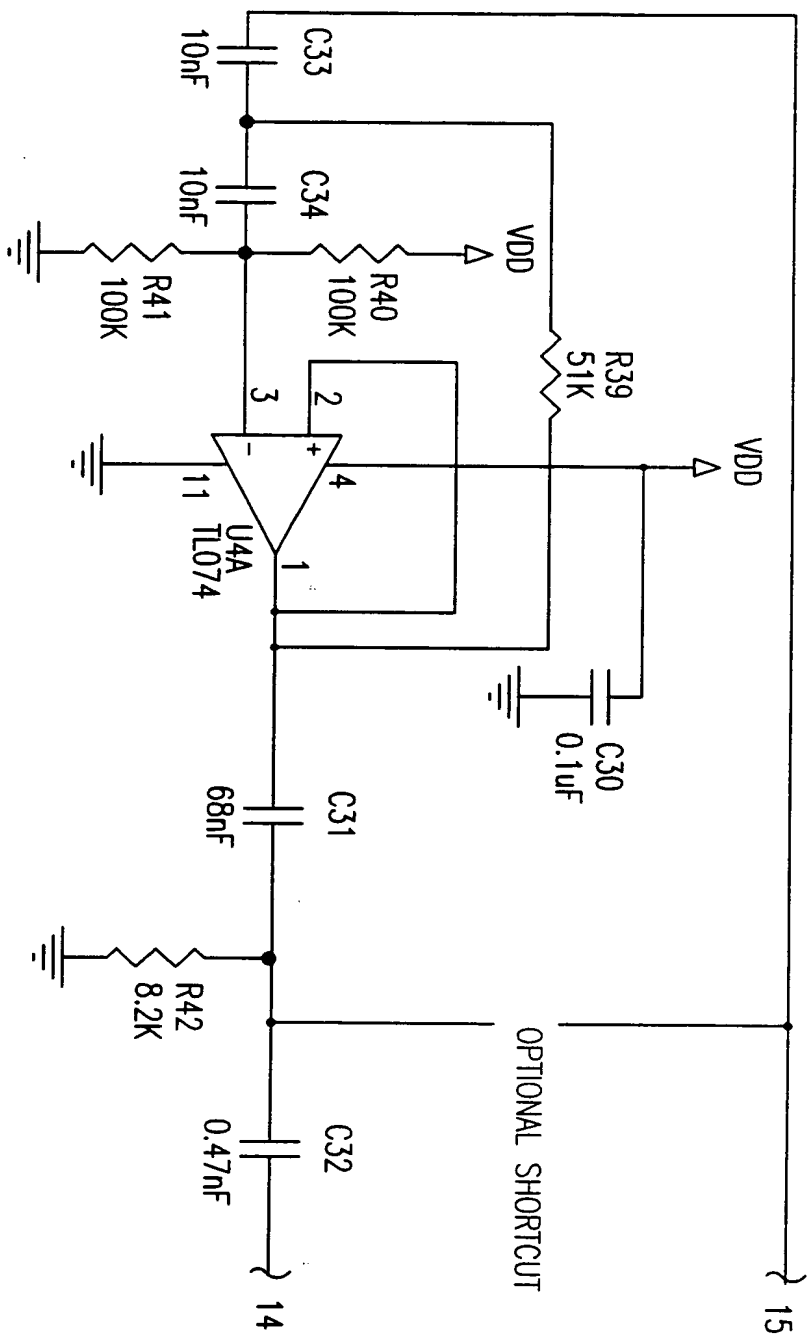


FIG. 9E/1

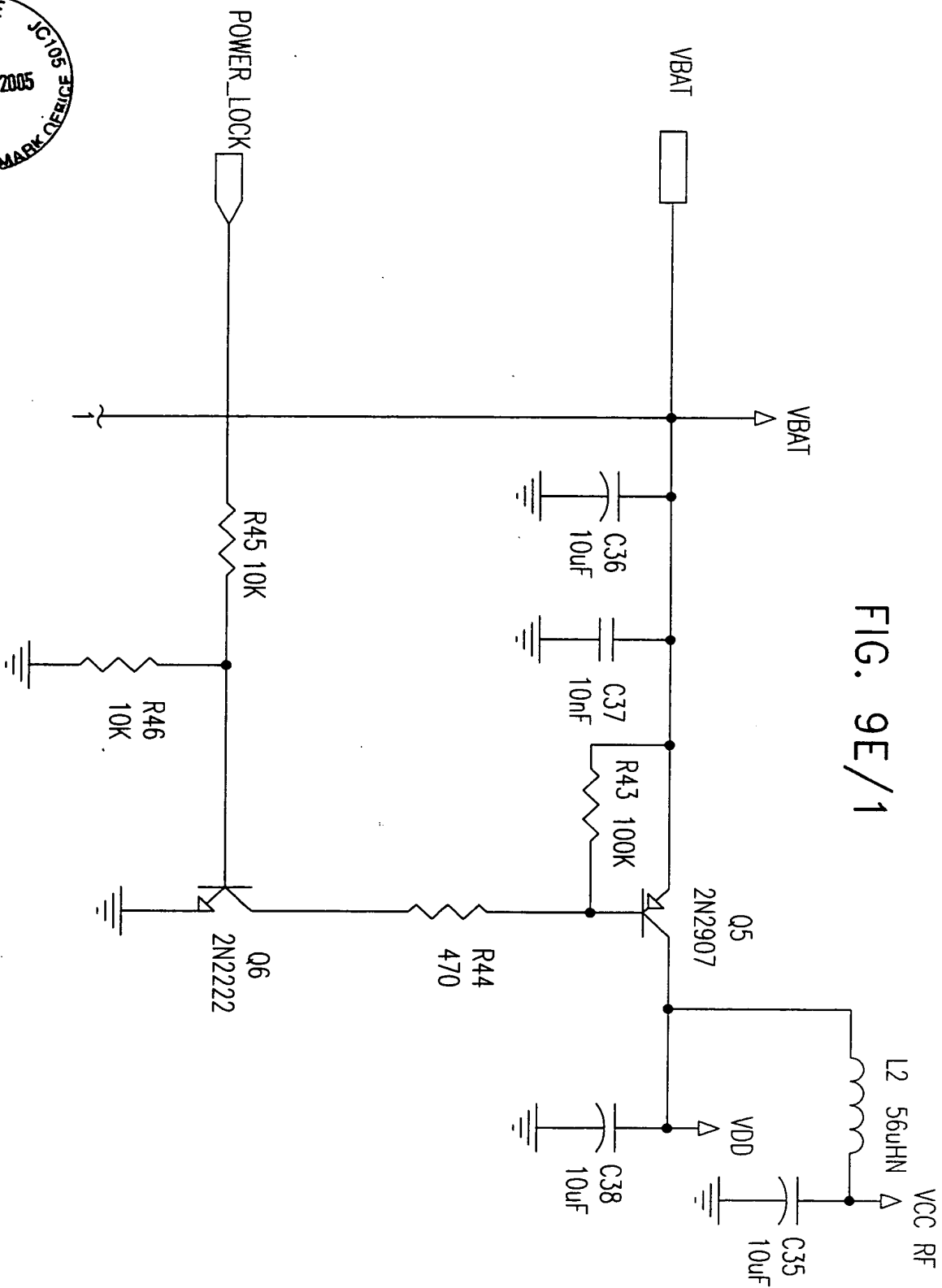






FIG. 9F

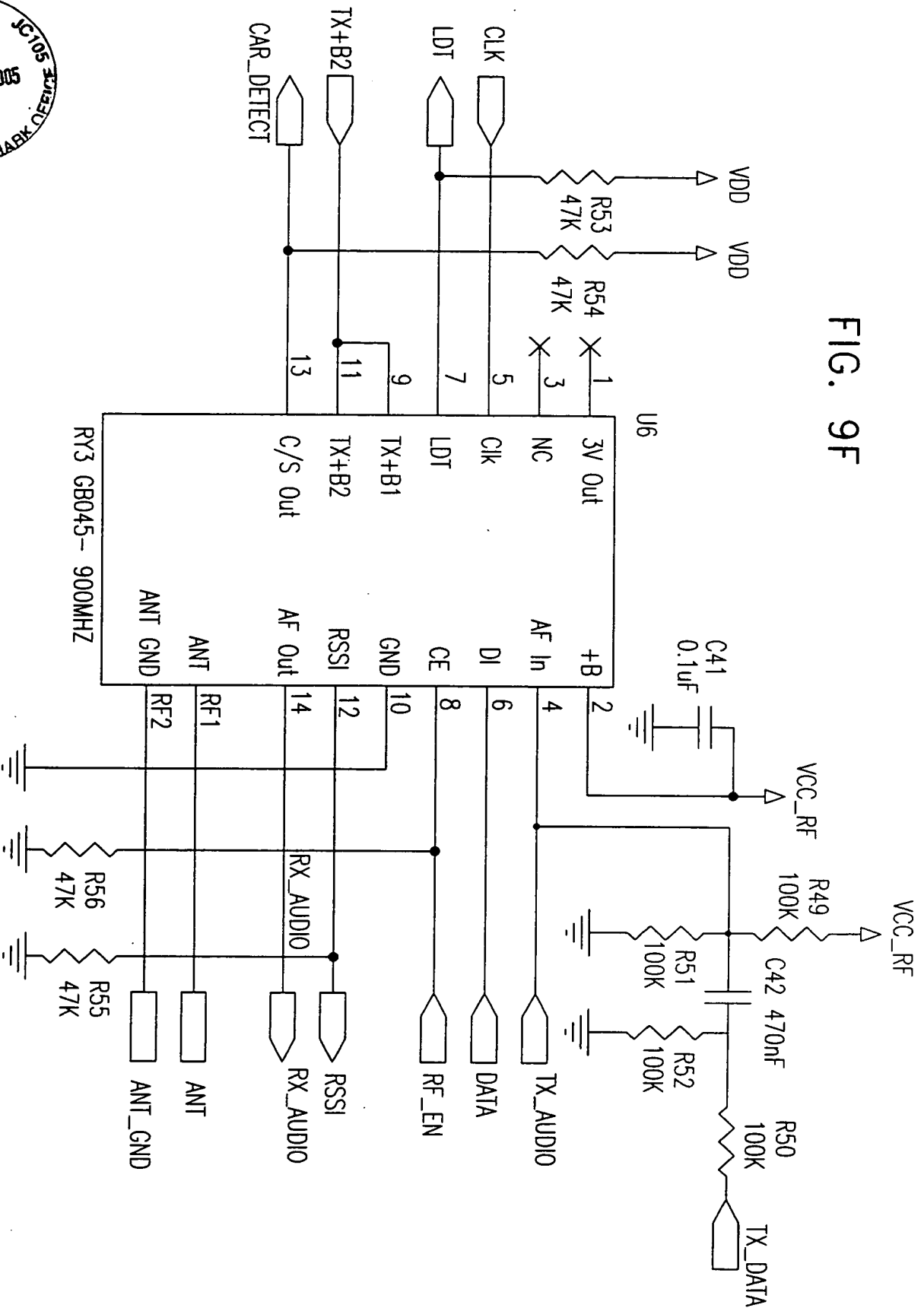


FIG. 9G

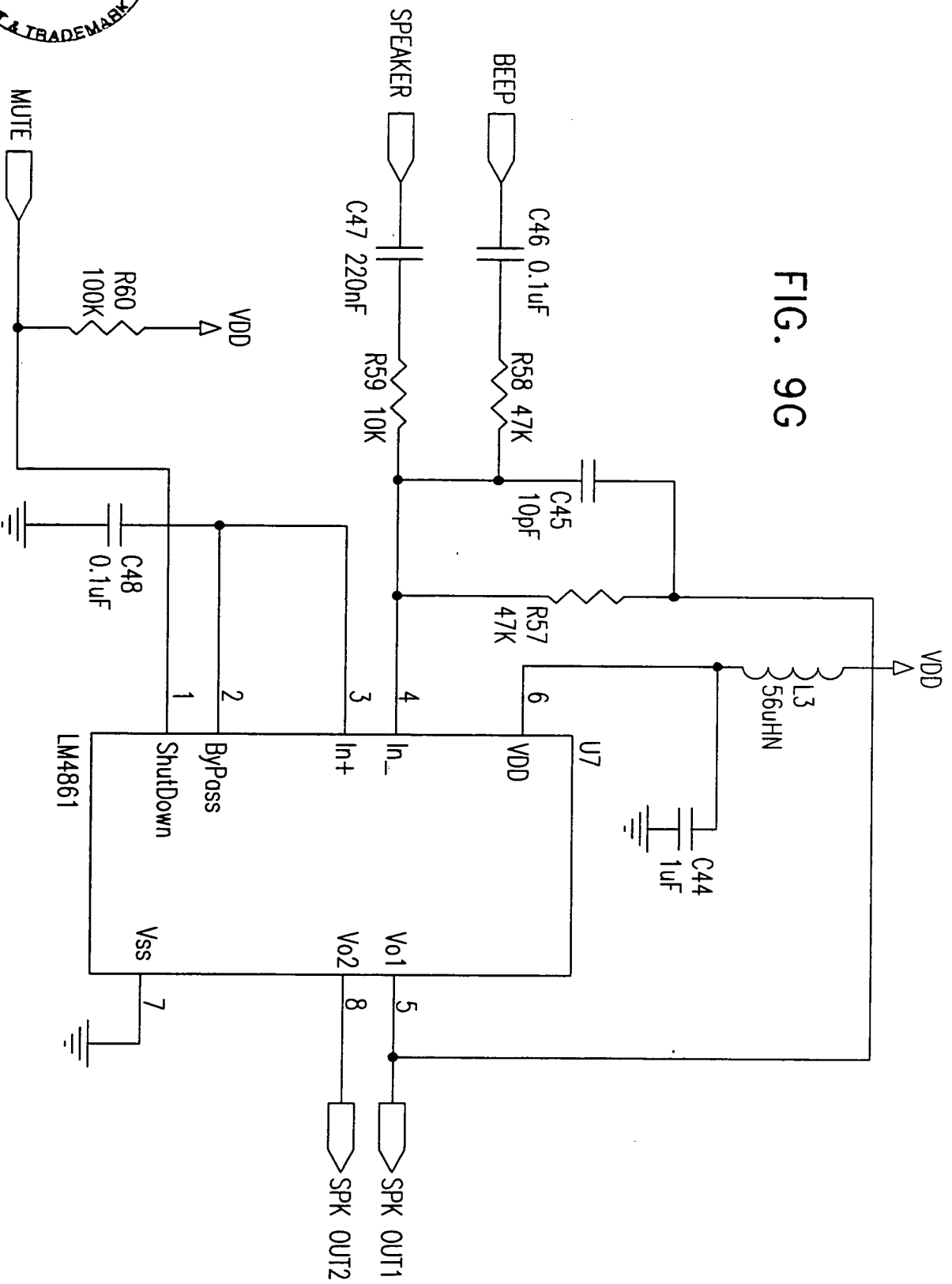


FIG. 9H/1

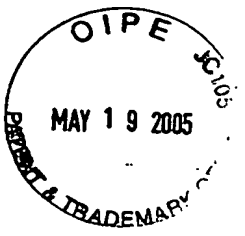
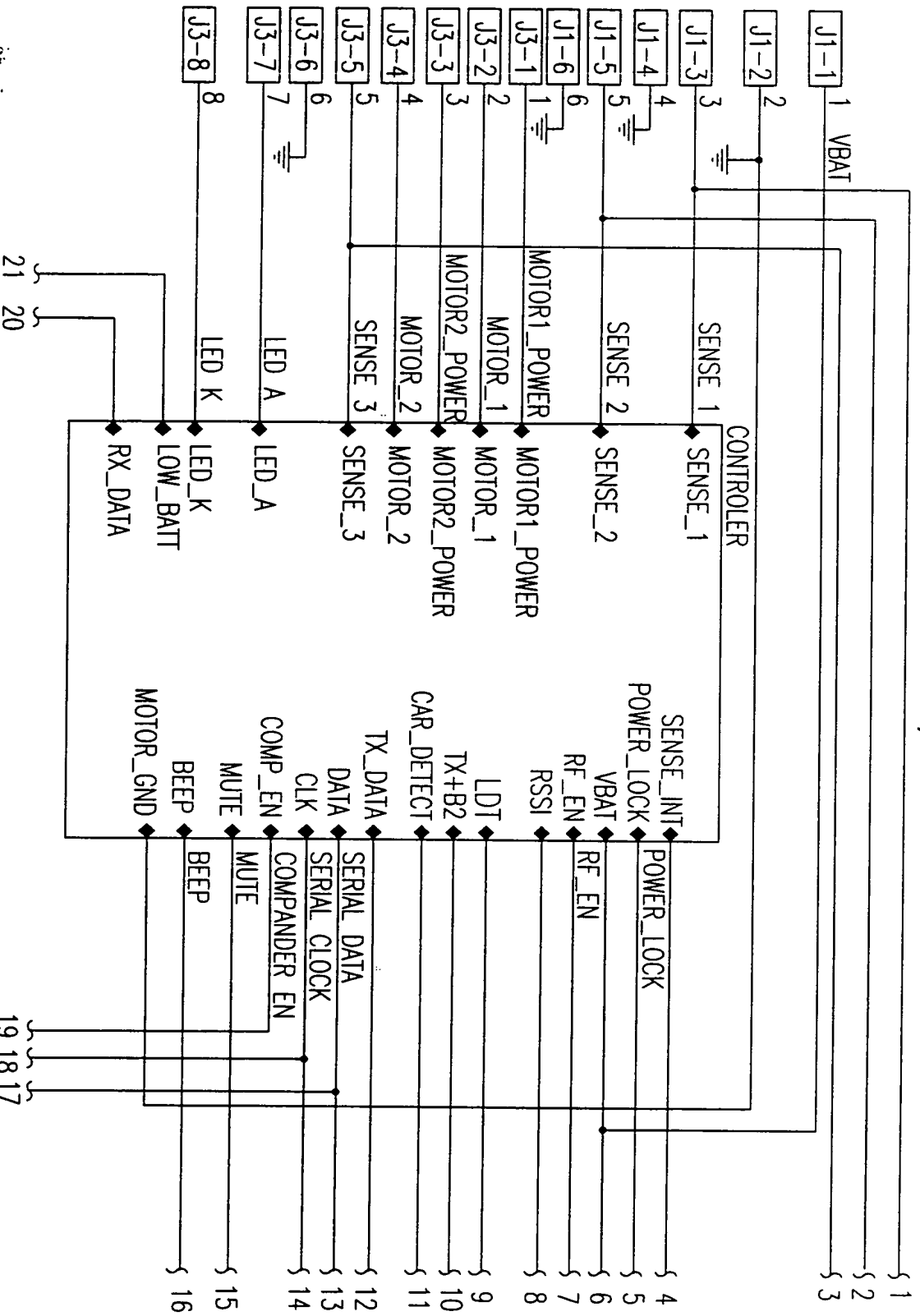


FIG. 9H/2

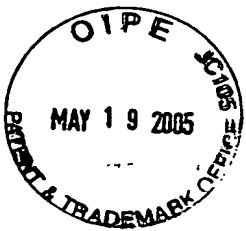
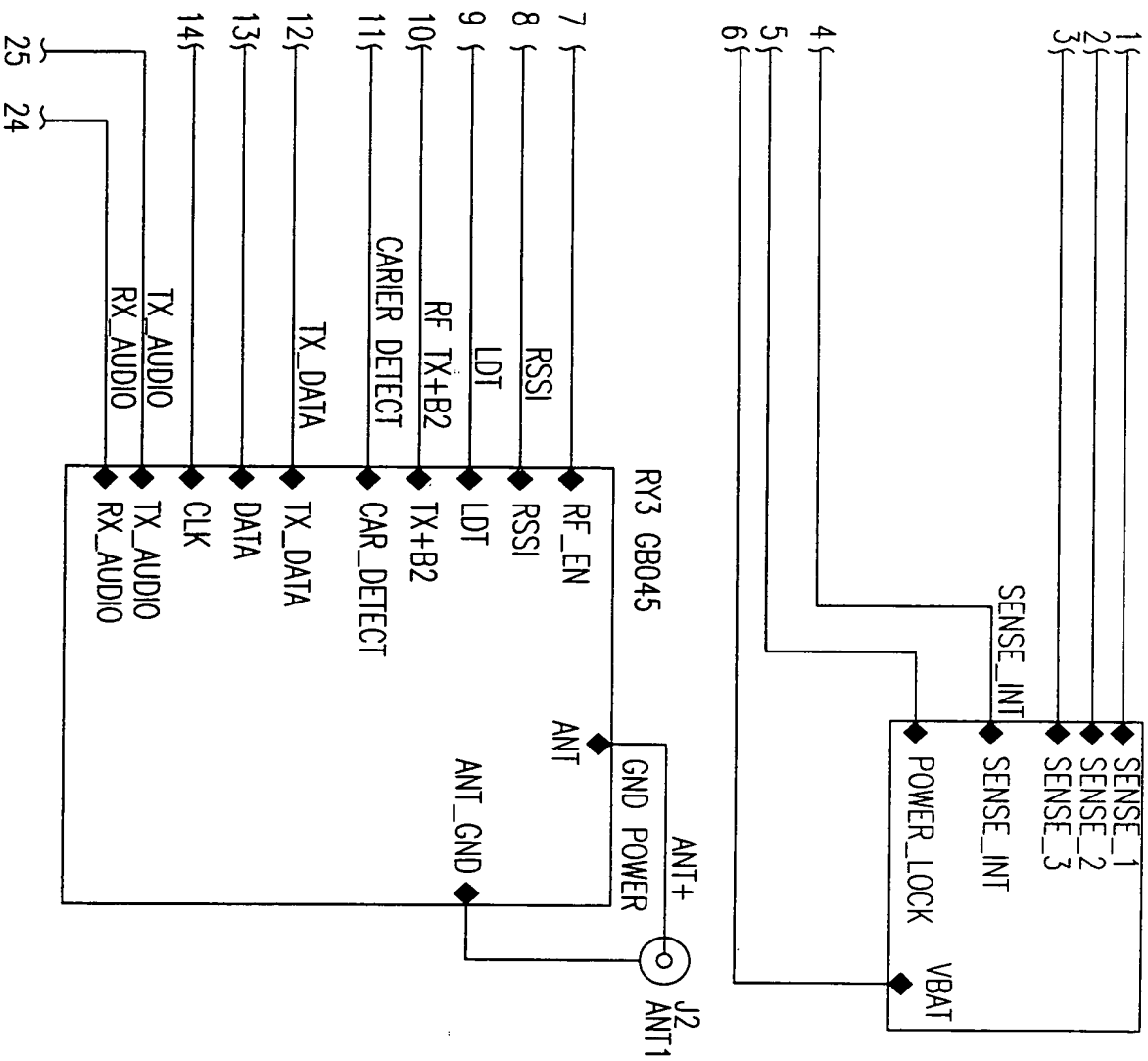


FIG. 9H/3

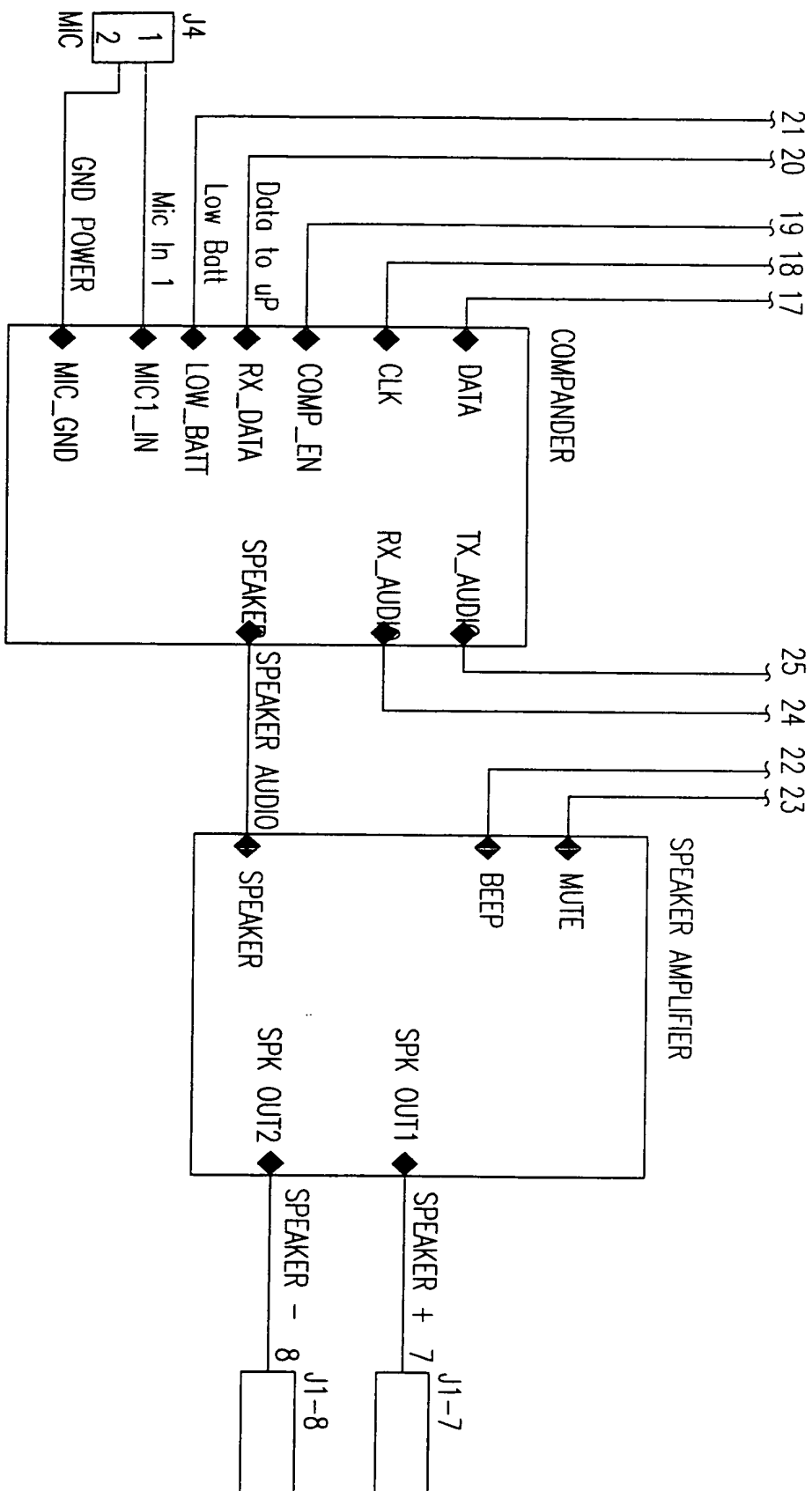
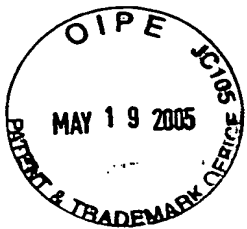
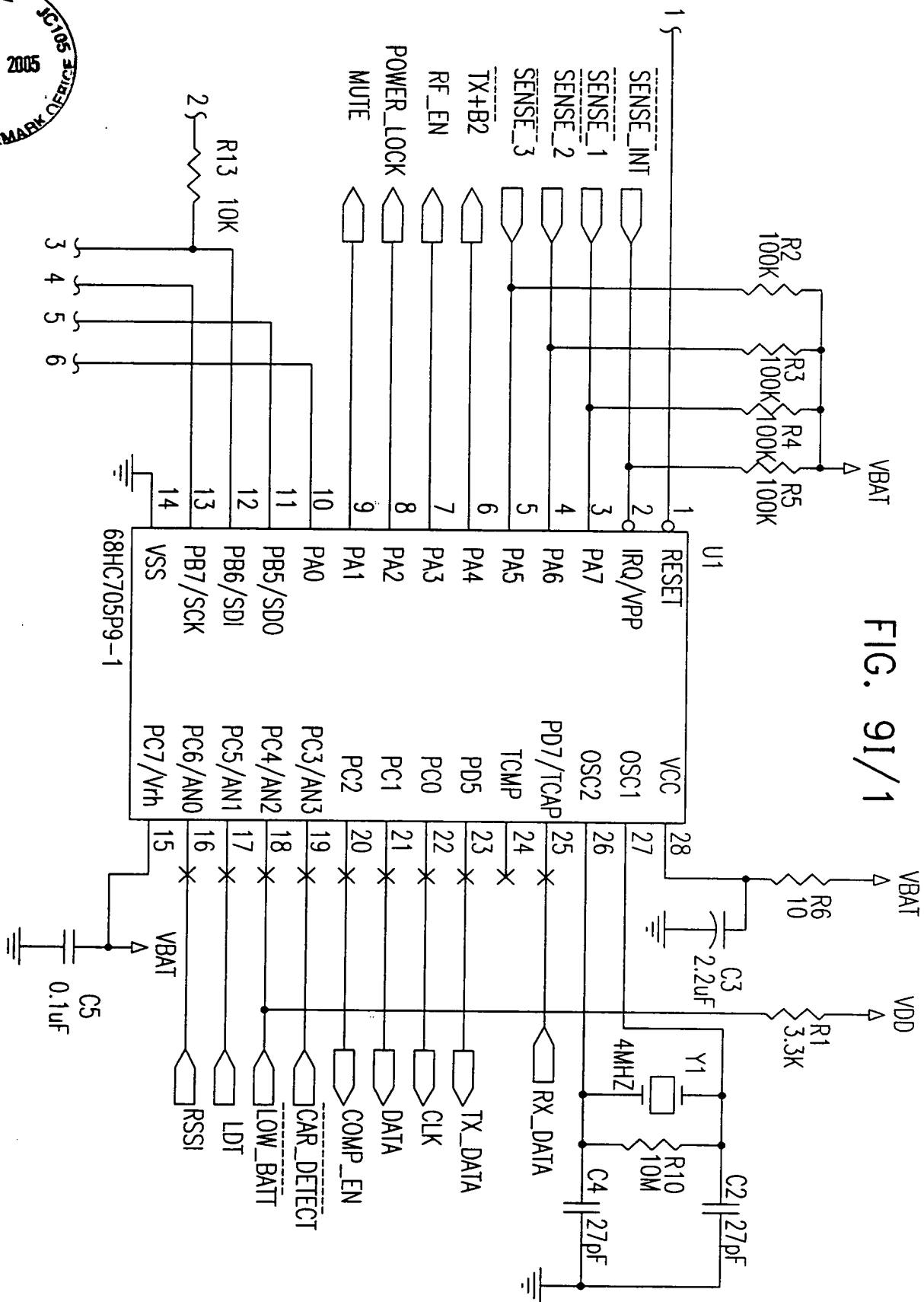


FIG. 91/1



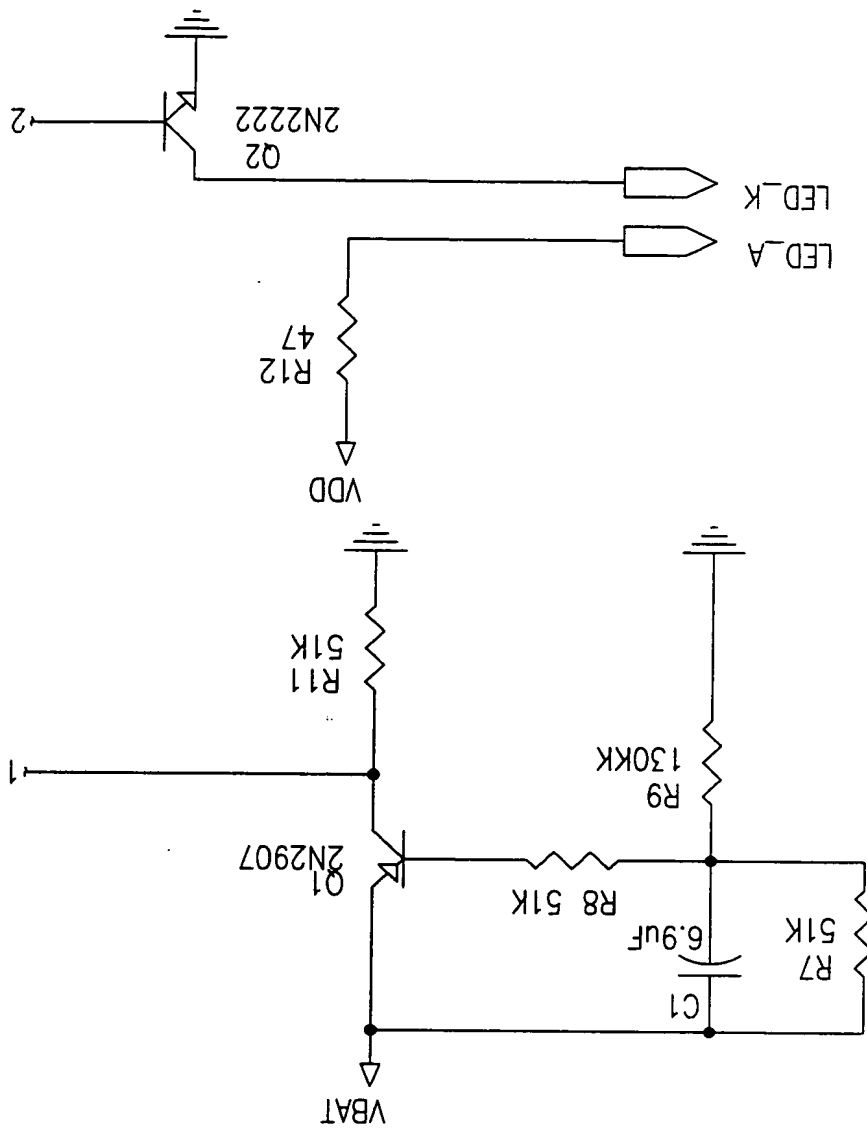
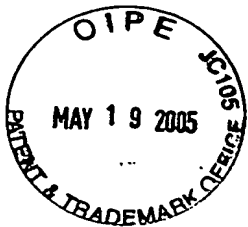


FIG. 91/2



FIG. 91/3

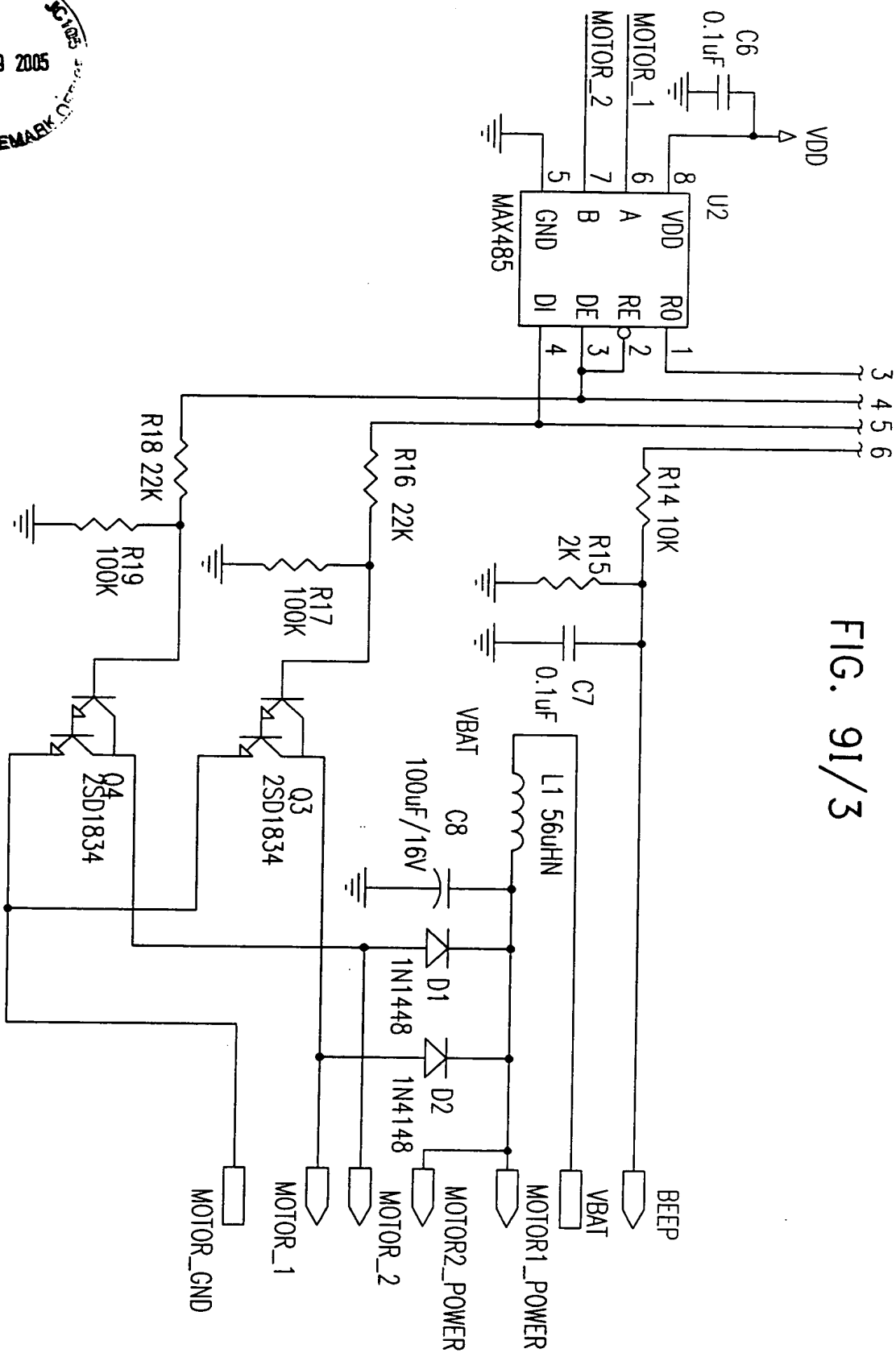
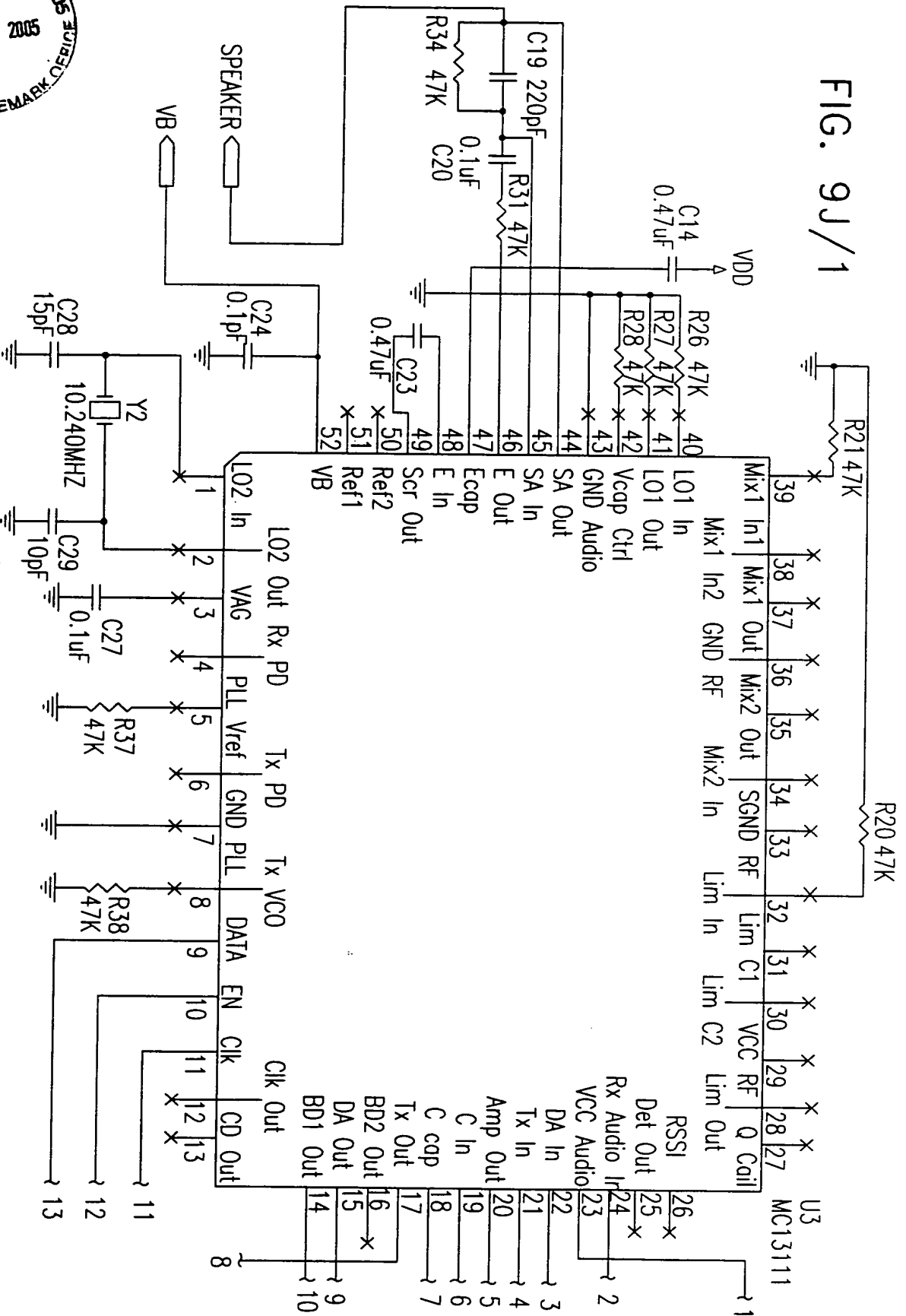


FIG. 9J/1



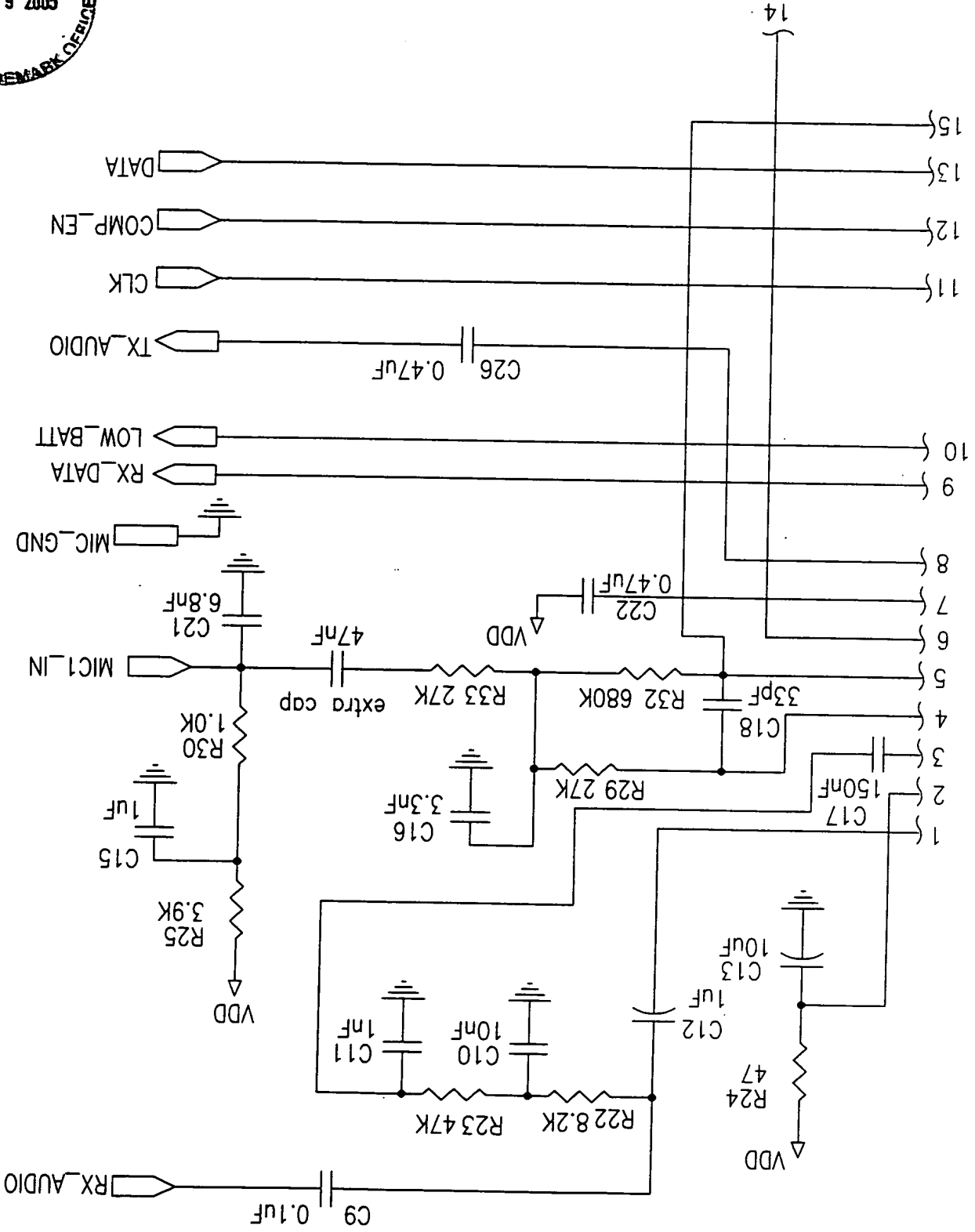
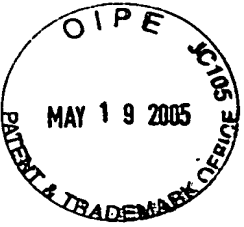


FIG. 9J/2

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FIG. 9J/3

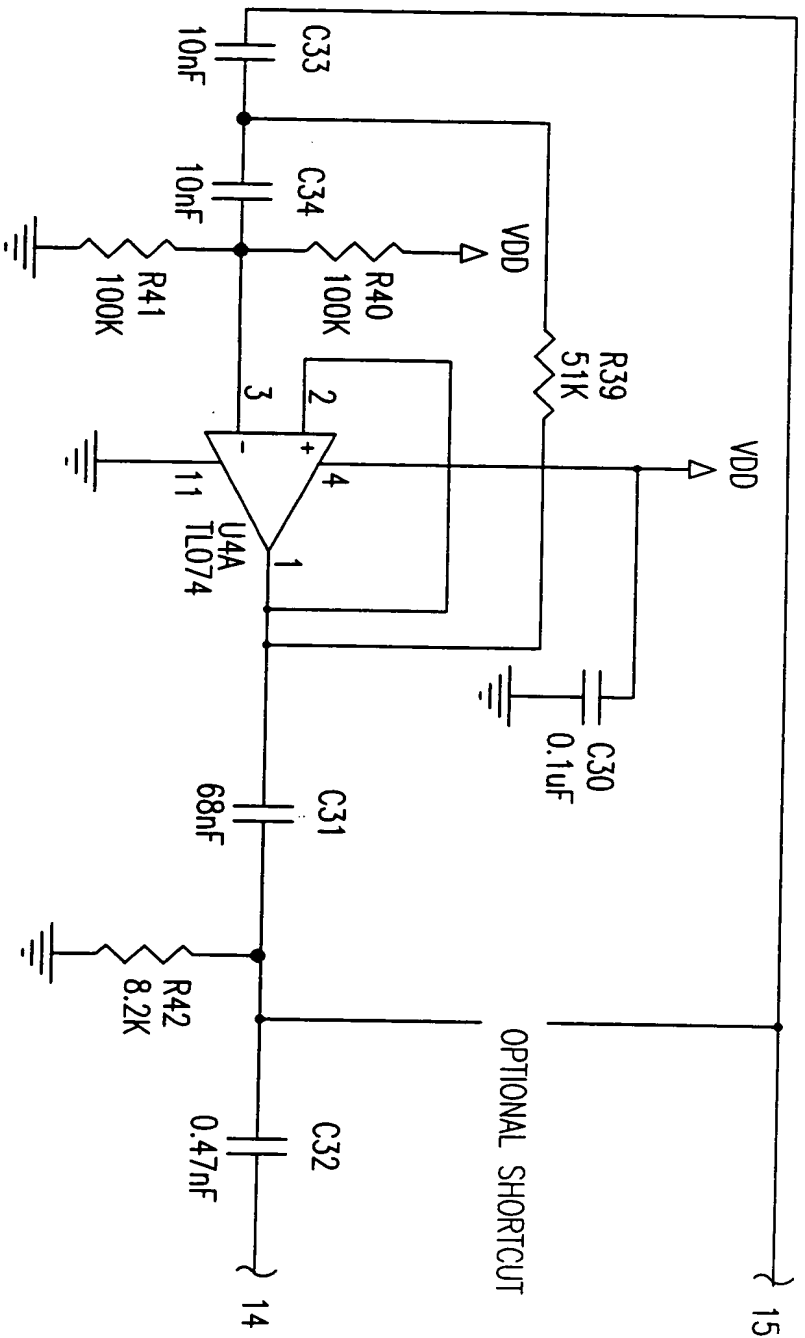


FIG. 9K/1

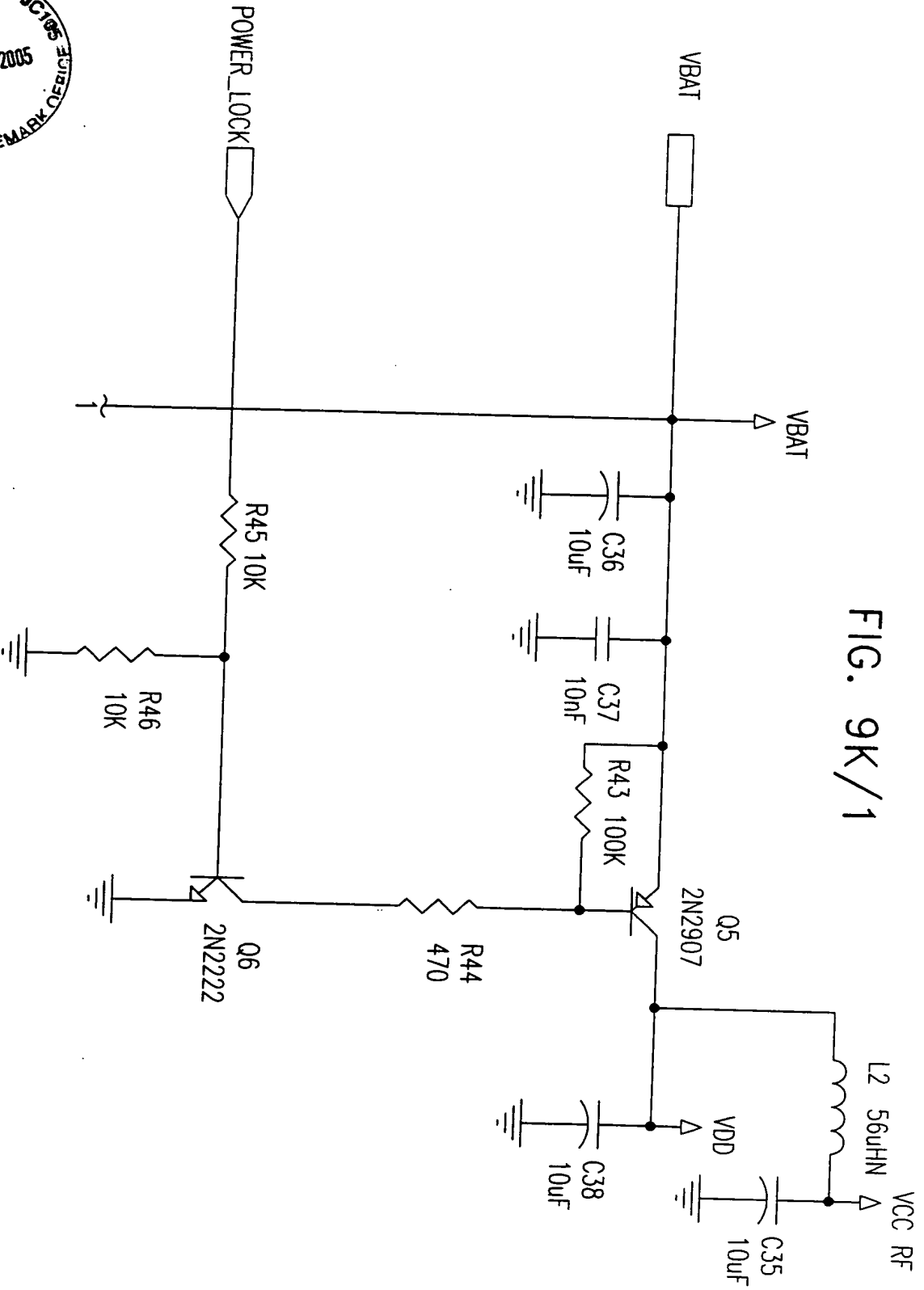


FIG. 9K/2

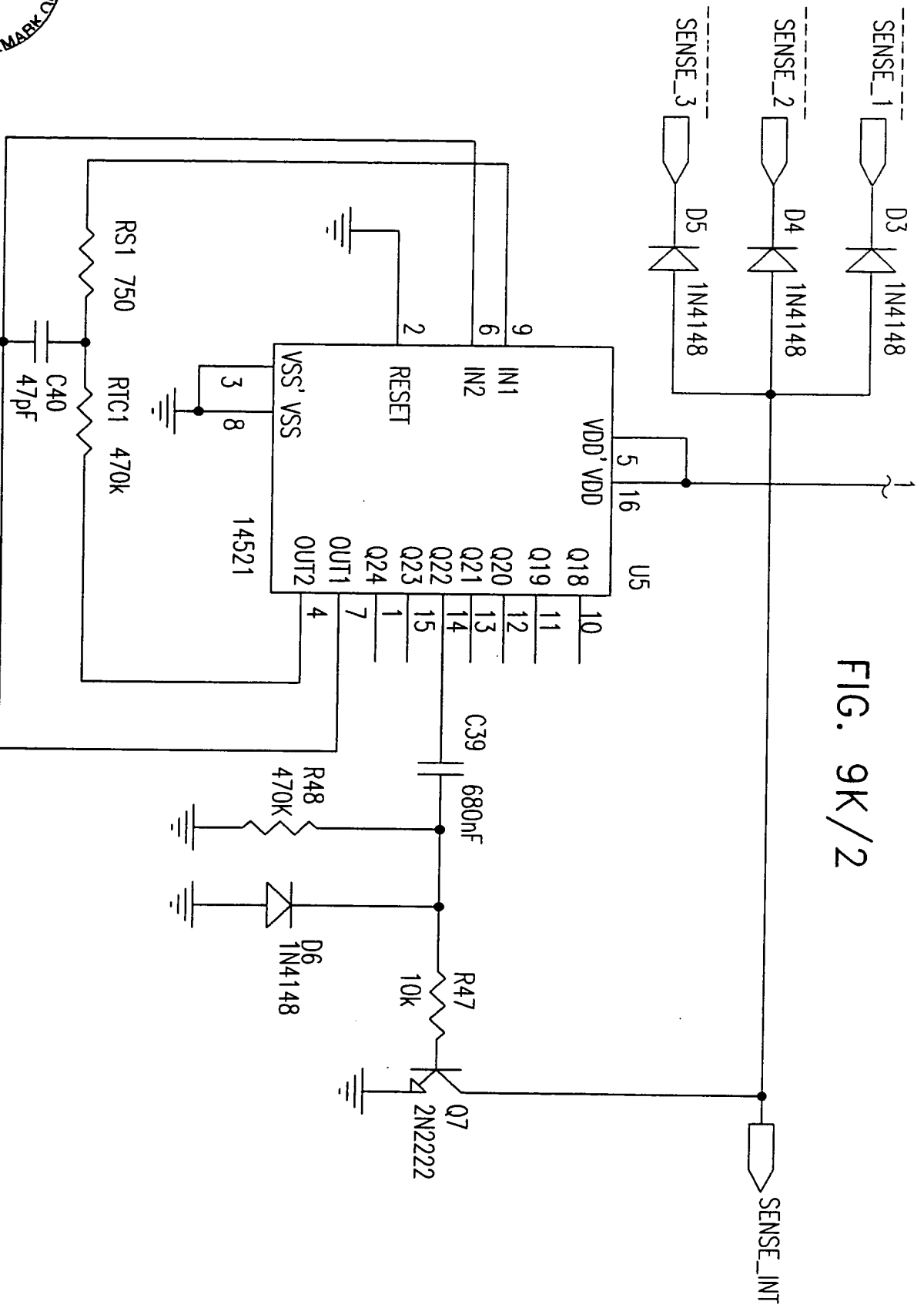


FIG. 9L

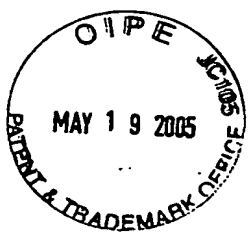
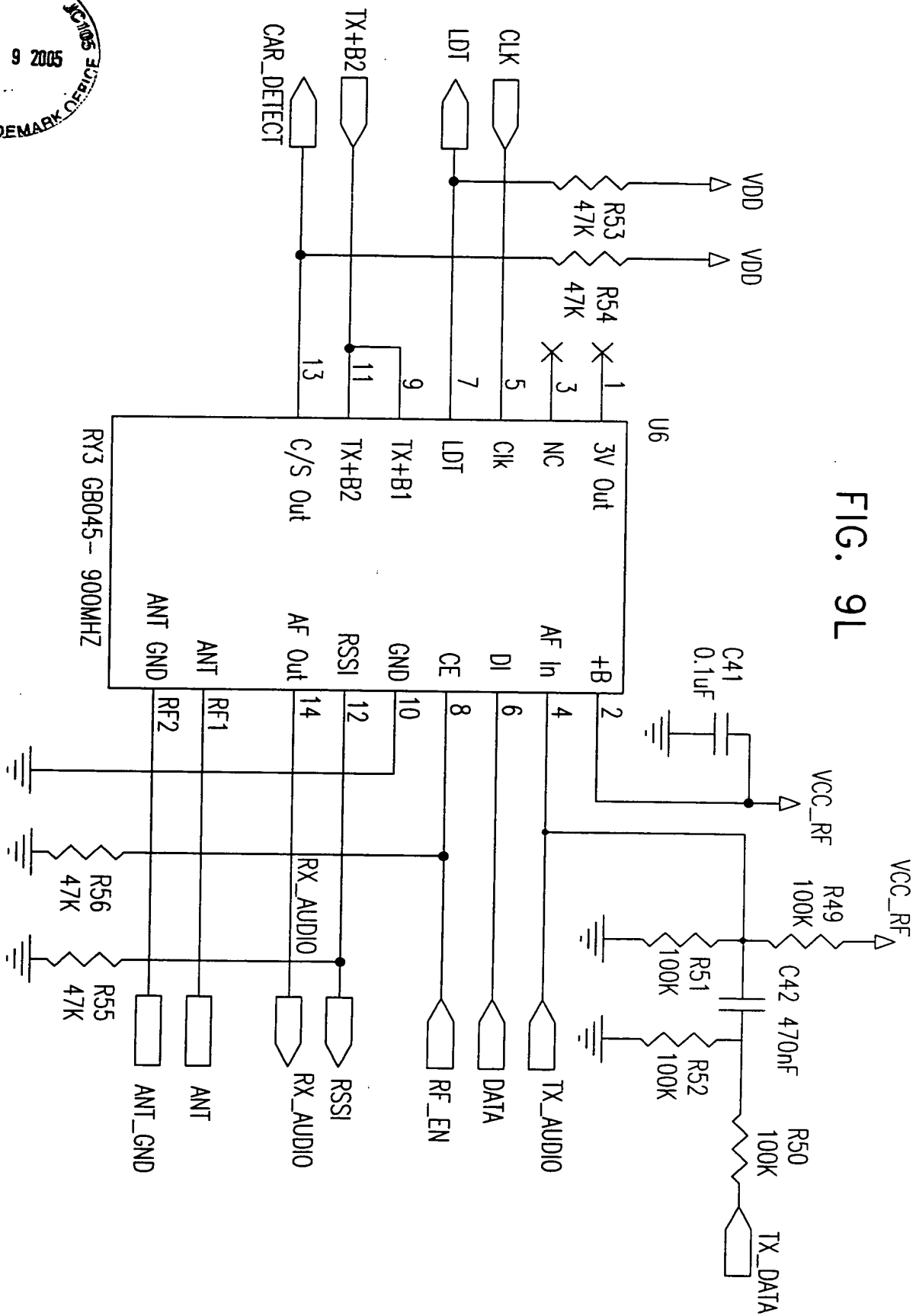


FIG. 9M

